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# ID ONE™ EPASS V2.1 WITH EAC CONFIGURATION RSA & ECC

## PUBLIC SECURITY TARGET

ISSUE: 1

### Verification and approval

| Function  | Name          | Visa  |
|---|---------------|---|
| Author / Security Certification Project manager | CAPEL Clément | <br>Approbation de FQR 110 4642 Ed1 par CAPEL Clément le 30-6-2009.oft |

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## 1 Security Target identification

### 1.1 Identification

General identification:

|             |   |
|-------------|---|
| Title:      | ID One™ ePass v2.1 with EAC configuration RSA & ECC |
| Editor:     | Oberthur Technologies                               |
| CC version: | 3.1 revision 2                                      |
| EAL:        | EAL4+   |

TOE technical identification<sup>1</sup>:

|                                |   |
|--------------------------------|---|
| Name:                          | ePass v2.1 on NXP P5CDXXX                                 |
| Microcontroller <sup>2</sup> : | P5CD040V0B, P5CD080V0B and P5CD144V0B of NXP <sup>3</sup> |
| ROM:                           | 069591  |
| Optional code:                 | 070942  |

### 1.2 Target of evaluation

The TOE is a Machine readable travel document implementing the Basic Access control as defined in [R1] and [R2] and the Extended Access Control as described in [R4].

<sup>1</sup> Some TOE identification data are stored in a file located in the chip EEPROM memory (see 3.2).

<sup>2</sup> The software is embedded on these three chips. Nevertheless, this not impacts the security target which remains the same.

<sup>3</sup> These chips are certified at EAL5+ according to the BSI-0002 Protection Profile.

## 2 Conformance claims

### 2.1 Common Criteria conformance

This Security Target (ST) is CC Part 2 extended [R31] and CC Part 3 conformant [R32] and written according to the Common Criteria version 3.1 Part 1 [R30].

### 2.2 Package conformance

This ST is conformant to the EAL4 package as defined in [R32].

:

The EAL4 have been augmented with the following requirements to fulfill the smartcards standard assurance level:

| Requirement | Name                                       | Type                          |
|-------------|--|-------------------------------|
| ALC_DVS.2   | Sufficiency of security measures           | Higher hierarchical component |
| AVA_VAN.5   | Advanced methodical vulnerability analysis | Higher hierarchical component |

#### Application note 1

For interoperability reasons it is assumed the receiving State cares for sufficient measures against eavesdropping within the operating environment of the inspection systems. Otherwise the MRTD may protect the confidentiality of some less sensitive assets (e.g. the personal data of the MRTD holder which are also printed on the physical MRTD) for some specific attacks only against enhanced basic attack potential (AVA\_VAN.3)<sup>4</sup>.

### 2.3 Protection Profile conformance

The Security Target is conformant to the following PP written in CC2.3:

- Machine readable travel documents with “ICAO Application”, Extended Access control [R8]

#### Remark:

Since this PP is not yet available in CC 3.1, requirements have been translated from CC2.1 to CC3.1 revision 2.

<sup>4</sup> AVA\_VLA.2 of CC2.3 has been translated to AVA\_VAN.3 in CC3.1r2. Therefore “low attack potential” has been translated to “enhanced basic attack potential”.

## 3 TOE Description

This part of the Security Target describes the TOE as an aid to the understanding of its security requirements. It addresses the product type, the intended usage and the main features of the TOE.

This part includes:

- Introduction
- TOE overview
- TOE on-chip identification
- TOE logical structure
- TOE life-cycle,
- Limits of the TOE
- TOE environment
- TOE scope

### 3.1 Introduction

The Target of Evaluation (TOE) is the contact-less integrated circuit chip of machine readable travel documents (MRTD's chip) programmed according to the Logical Data Structure (LDS) [R4] and providing the Basic Access Control according to the ICAO document [7] and the Extended Access control according to [29].

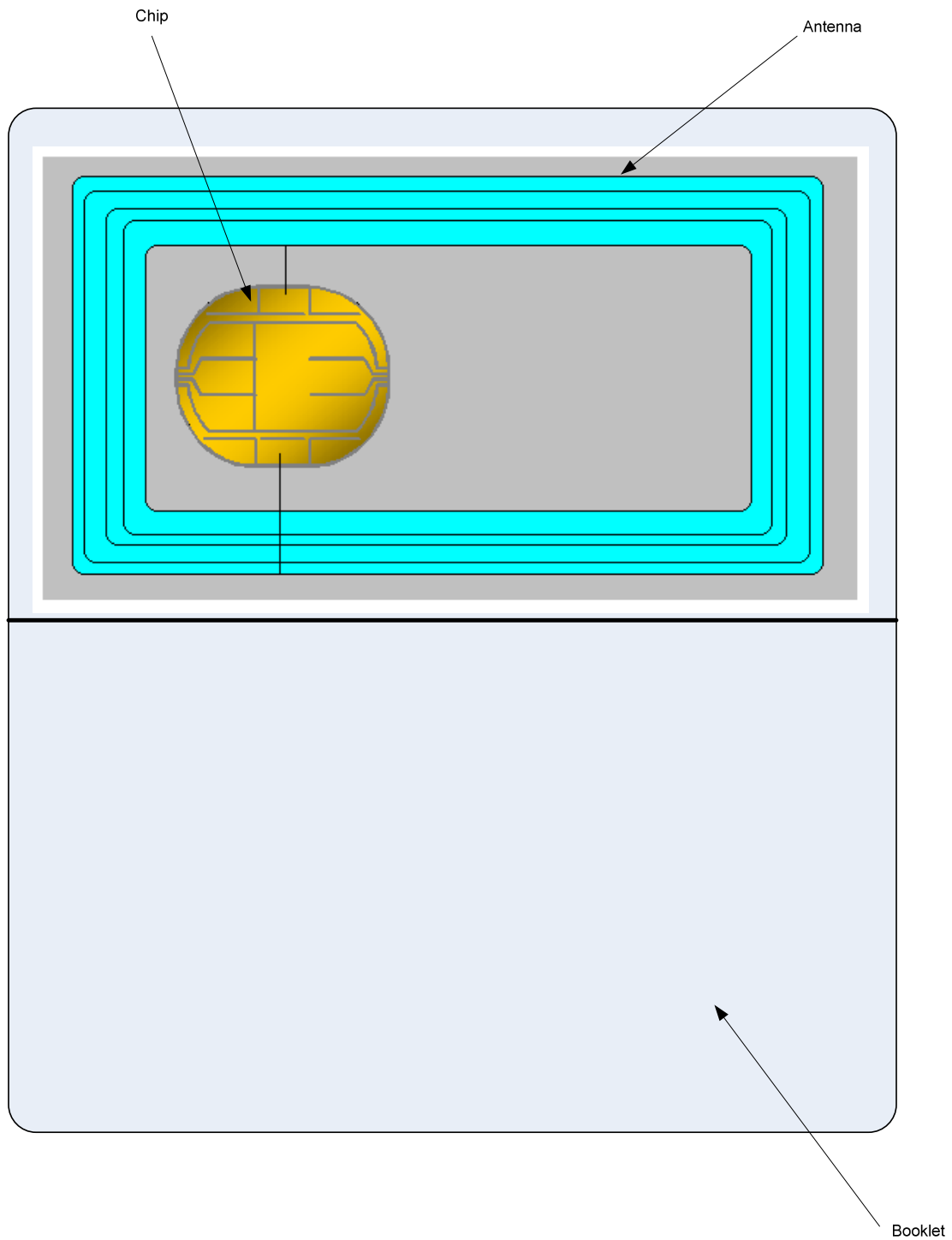
The TOE comprises of:

- The circuitry of the MRTD's chip (the integrated circuit: IC) with hardware for the contact-less interface, e.g. antennae, capacitors,
- The IC Dedicated Software with the parts IC Dedicated Test Software and IC Dedicated Support Software,
- The IC Embedded Software (operating system: OS) loaded on the ROM
- The optional code loaded on EEPROM
- The MRTD application
- The associated guidance documentations.

**NB:** Although it is included in the TOE, integration of the IC with the booklet and antenna is out of the evaluation scope (see [R9]).

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**Figure 1 : Physical TOE overview**

### 3.2 TOE on-chip Identification

At any time TOE can be identified by reading the CPLC data stored in ROM as specified in the AGD\_PRE document.

The optional code, the ROM code, the configuration of the TOE, as well as the PP claim can also be identified using the dedicated file EF.TOE\_Identification, in which all the relevant data are stored in phase 2.

```
EF.TOE_Identification ::= SEQUENCE_OF{  
  
    BYTE STRING ROMCodeIdentifier  
    BYTE STRING OptionalCodeIdentifier  
    BYTE STRING PPIIdentifier  
    BYTE STRING CertificateIdentifier  
    BYTE STRING ProprietaryData  
  
}
```

In which

- ROMCodeIdentifier = 069591
- OptionalCodeIdentifier = 070942

PPIIdentifier and CertificateIdentifier are set according to the AGD\_PRE document.

### 3.3 TOE usage and security features for operational use

State or organisation issues MRTD to be used by the holder for international travel. The traveller presents a MRTD to the inspection system to prove his/her identity. The MRTD in context of this security target contains:

- Visual (eye readable) biographical data and portrait of the holder printed in the booklet
- A separate data summary (MRZ data) for visual and machine reading using OCR methods in the Machine readable zone (MRZ)
- And data elements stored on the MRTD's chip for contact-less machine reading.

The authentication of the traveller is based on:

- The possession of a valid MRTD personalized for a holder with the claimed identity as given on the biographical data page and
- The Biometric matching performed on the Inspection system using the reference data stored in the MRTD.

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The issuing State or Organization ensures the authenticity of the data of genuine MRTD's. The receiving State trusts a genuine MRTD of an issuing State or Organization.

For this security target the MRTD is viewed as unit of:

- (a) The **physical MRTD** as travel document in form of paper, plastic and chip. It presents visual readable data including (but not limited to) personal data of the MRTD holder
  - (1) The biographical data on the biographical data page of the passport book,
  - (2) The printed data in the Machine-Readable Zone (MRZ) and
  - (3) The printed portrait.
  
- (b) The **logical MRTD** as data of the MRTD holder stored according to the Logical Data Structure [R4] as specified by ICAO on the contact-less integrated circuit. It presents contact-less readable data including (but not limited to) personal data of the MRTD holder
  - (1) The digital Machine Readable Zone Data (digital MRZ data, DG1),
  - (2) The digitized portraits (DG2),
  - (3) The optional biometric reference data of finger(s) (DG3) or iris image(s) (DG4) or both
  - (4) The other data according to LDS (DG5 to DG16) and
  - (5) The Document security object.

The issuing State or Organization implements security features of the MRTD to maintain the authenticity and integrity of the MRTD and their data. The MRTD as the passport book and the MRTD's chip is uniquely identified by the document number.

The physical MRTD is protected by physical security measures (e.g. watermark on paper, security printing), logical (e.g. authentication keys of the MRTD's chip) and organisational security measures (e.g. control of materials, personalisation procedures). These security measures include the binding of the MRTD's chip to the passport book.

The logical MRTD is protected in authenticity and integrity by a digital signature created by the document signer acting for the issuing State or Organization and the security features of the MRTD's chip.

The ICAO specifications [R1] & [R2] define the baseline security methods such as the Passive Authentication and the Basic Access Control to protect the data retrieval. These two features are mandatory.

The Basic Access Control is a security feature that is supported by the TOE. The inspection system

- (i) reads the printed data in the MRZ
- (ii) authenticates themselves as inspection system by means of keys derived from MRZ data. After successful authentication of the inspection system the MRTD's chip provides read access to the logical MRTD by means of private communication (secure messaging) with this inspection system.

The Active Authentication of the MRTD's chip (described in [R1]) is an optional feature that may be implemented. It ensures that the chip has not been substituted, by means of a challenge-response

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protocol between the inspection system and the MRTD's chip. For this purpose the chip contains its own Active Authentication RSA or ECC Key pair. A hash representation of Data Group 15 Public Key is stored in the Document Security Object (SOD) and therefore authenticated by the issuer's digital signature. The corresponding Private Key is stored in the chip's secure memory.

The TOE supports the loading and generation of the Active Authentication RSA or ECC Key pair.

The Extended Access Control (defined in [R4]) enhances the later security features and ensures a strong and mutual authentication of the passport and the Inspection system. This step is required to access the biometric data such as the fingerprint and/or the iris. In particular, the authentication steps ensures a strong secure channel able to provide confidentiality of the biometric data that are read and authentication of the Inspection system retrieving the data to perform a Match on Terminal comparison. The Extended Access Control authentication steps the TOE implements may be realized either with elliptic curve cryptography or with RSA cryptography.

This security target addresses the following security features of the logical MRTD:

- (i) Protection in integrity by write only-once access control and by physical means
- (ii) Authentication between the passport holder and the Inspection system prior to any border control by the Basic Access Control Mechanism
- (iii) Protection in integrity and confidentiality of data read by the secure messaging
- (iv) Authentication of the genuine chip by the Active Authentication mechanism (if activated)
- (v) Strong authentication of the chip and the Inspection system prior to any biometric data retrieval

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### 3.4 TOE logical structure

The TOE contains an application embedded in the chip. This application fulfils the requirements described beforehand and in [R1], [R2], [R4].

This application is made of:

- A file system compliant with [R13]
- A software, executing operation to protect the files (some) and using data stored within the files (some)
- Other data structure that are not files

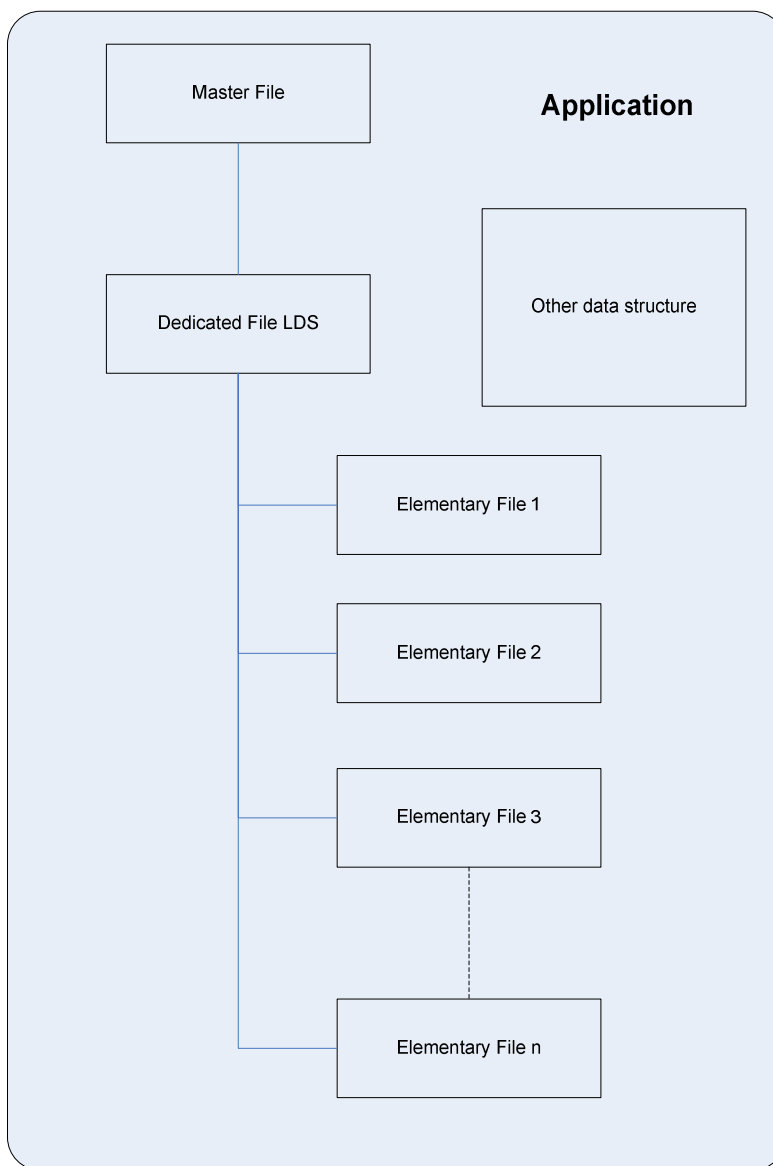
Roughly, the embedded application, when powered, is seen as a master file, containing a Dedicated file (DF) for the LDS.

This dedicated file is selected by means of the Application Identifier (AID) of the LDS application.

Once the LDS dedicated files are selected, the file structure it contains may be accessed, provided the access conditions are fulfilled.

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Structure of the File system

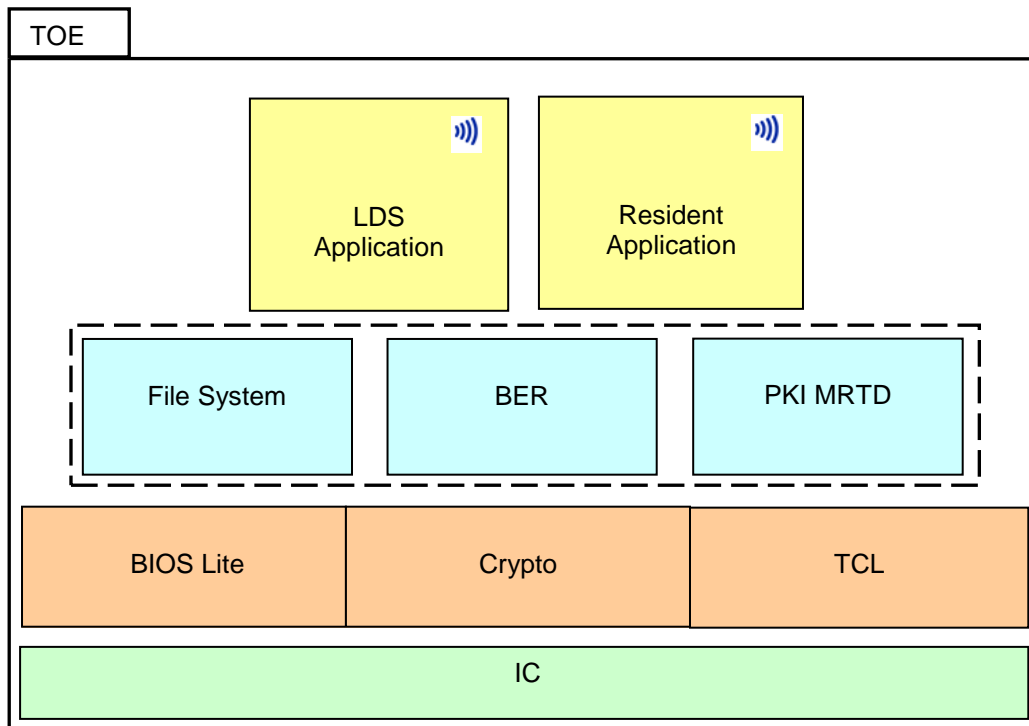



**Figure 2 : Structure of the File system**

### 3.4.1 Software Architecture of the TOE

The Figure below shows the logical structure of the TOE, showing the layered architecture used to combine the subsystems lightly describe below:

|                       |                    |                          |              |
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 This layer is permeable. This means that upper layers can also directly access to lower layer for a defined set of services.

**Figure 3 : Logical structure of the TOE**

- **LDS application:** This “application” fulfils the following functionalities:
  - Implements the commands of e-passport that are available in operational phase
  - Manages access control on these commands
  - Implements authentication mechanisms:
    - Basic Access Control (BAC), including session keys generation ,
    - Active Authentication (AA),
    - Extended Access control (EAC).
- **Resident application:** This “application” fulfils the following functionalities,
  - Implements the commands of e-passport that are available in pre-personalisation and personalisation phases,
  - Manages access control on these commands.
- **API layer (blue):** This layer provide generic APIs used by applications,
  - **File System** implements the secure management of application data for the applications,
  - **BER** provides a toolbox to manage APDU format,
  - **PKI MRTD** Implements generic functions for Activation Authentication and Secure Messaging of incoming and outgoing commands.
- **Low level layer (orange):** This layer provides an interface between the Hardware and upper layer.

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- **BIOS Lite** performs access to memory (read & write) – EEPROM - and other basic manipulation of the hardware,
  - **Crypto** provides cryptographic services such as
    - 3DES,
    - Random Number generator,
    - RSA,
    - Elliptic curves cryptography (ECDSA and ECDH),
    - Message Digest Computation (SHA-1, SHA-224, SHA-256,SHA-384)
  - **TCL** handles the communication interface (i.e. conctless interface).
- **IC layer:** it corresponds to the chip features.

### 3.5 TOE life cycle according to the PP 9911

The Smart card life-cycle considered hereby, is the one described in [R28]. This protection profile is decomposed into 7 phases, described hereafter.

This life cycle is related to the different phases the designer/manufacturer/issuer has to go through to get a smart card ready to use. It starts from the design till the end of usage of the card.

It is depicted in the figure below:

|                       |                    |                          |              |
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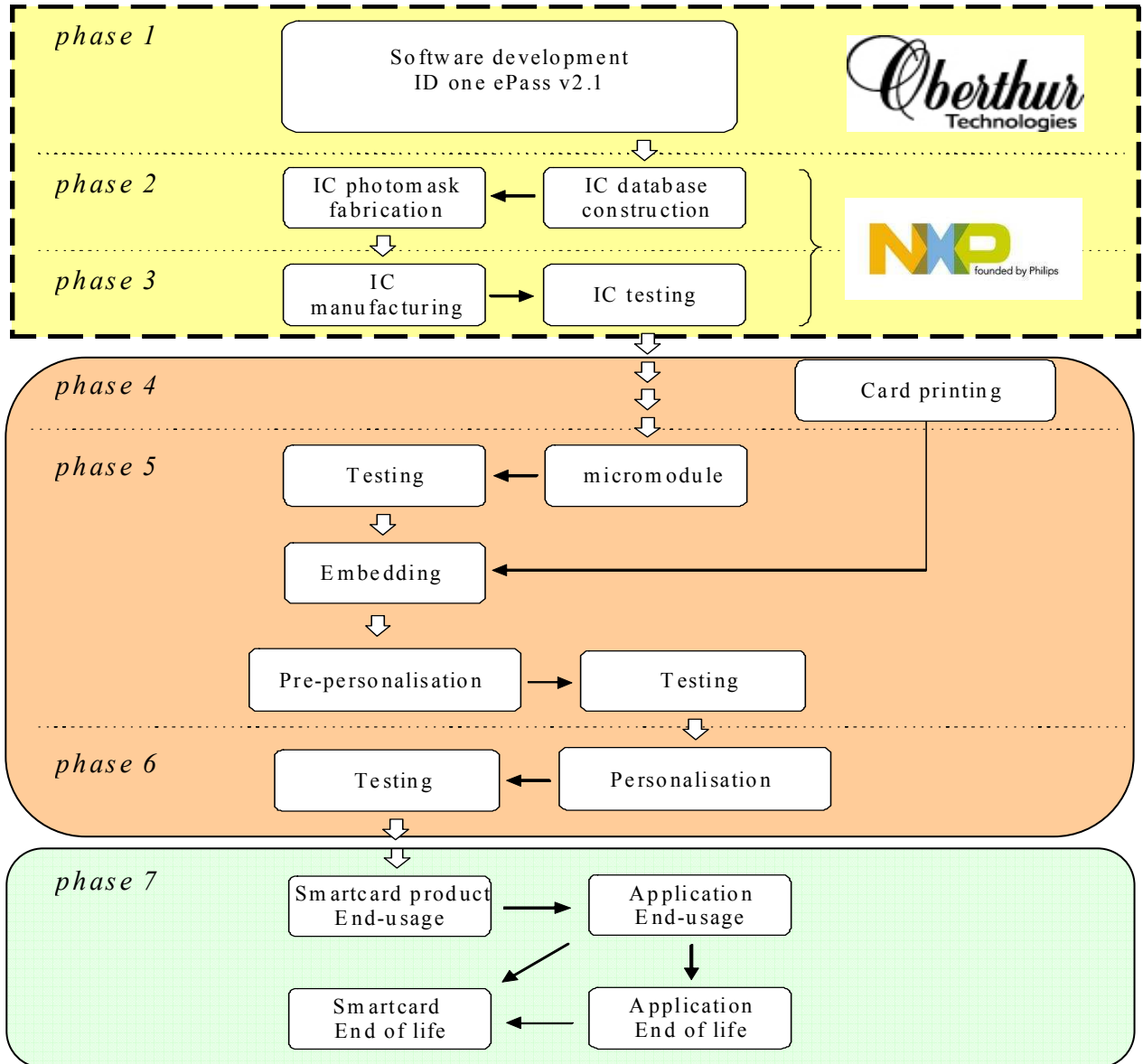
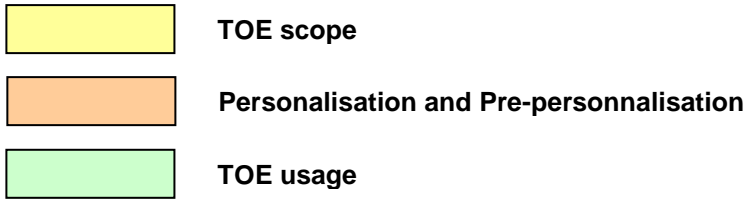


Figure 4 Smartcard product life-cycle for the TOE

### 3.6 Description of the TOE environment

The TOE environment may be spitted into three different parts:

- The **development environment**, in which the TOE is designed, tested and manufactured. The security requirements that are applied reach the one described in [R7], [R8] and [R6].
- The **production environment** in which the TOE is tested and manufactured. The security requirements that are applied reach the one described in [R7], [R8] and [R6].
- The **User environment**, in which the TOE is used as stated in [R7], [R8] and [R6]. The security requirements that are requested and the assurance levels are met.

#### 3.6.1 Development environment

##### 3.6.1.1 Software development (phase 1)

This environment is enforced by OBERTHUR TECHNOLOGIES.

To ensure security, access to development tools and products elements (PC, emulator, card reader, documentation, source code...) is protected. The protection is based on measures for prevention and detection of unauthorized access. Two levels of protection are applied:

- Access control to OBERTHUR TECHNOLOGIES offices and sensitive areas.
- Access to development data through the use of a secure computer system to design, implement and test software

##### 3.6.1.2 Hardware development (Phase 2)

The environment is enforce by NXP site

The IC development environment is described in [R10], [R11] and [R12].

The ICs are certified EAL5+ and the IC certificate reference are:

- BSI-DSZ-CC0404-2007 for the NXP P5CD0040V0B
- BSI-DSZ-CC0417-2008 for the NXP P5CD0080V0B
- BSI-DSZ-CC0411-2007 for the NXP P5CD0144V0B

#### 3.6.2 Production environment

##### 3.6.2.1 IC manufacturing (phase 3)

The environment is equivalent to the one of the phase 2.

Depending on the choice made for the optional code loading, the optional code may be loaded during this phase.

##### 3.6.2.2 TOE manufacturing (phase 4 & 5)

Two situations may occur:

- The TOE may be manufactured by **OBERTHUR TECHNOLOGIES** in any of its manufacturing site
- The TOE may be manufactured at a **contractor's site**

The production sites present adequate security measures that fit the TOE protection during its manufacturing even if they are not in the scope of security assurance requirements for the environment.

More precisely, all the guidance for initialization, pre-personalisation and personalisation are applied with respect to **P.Manufact**.

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If **OBERTHUR TECHNOLOGIES** is in charge of manufacturing the TOE, the following process will be applied:

- Loading the optional code
- Loading the authentication key of the Personalisation Agent.
- Preparing the TOE prior to delivering it to the Personalisation Agent (phase 6)

If a **contractor** is in charge of manufacturing the TOE, the following process will be applied

- Loading the optional code – This step is optional; it may be performed by **OBERTHUR TECHNOLOGIES**
- Loading the authentication key of the personalisation Agent.
- Preparing the TOE prior to delivering it to the Personalisation Agent (phase 6)

The **OBERTHUR TECHNOLOGIES** manufacturing sites have all the needed certifications:  
The same level of security will be required for the contractor's site.

**Remark:**

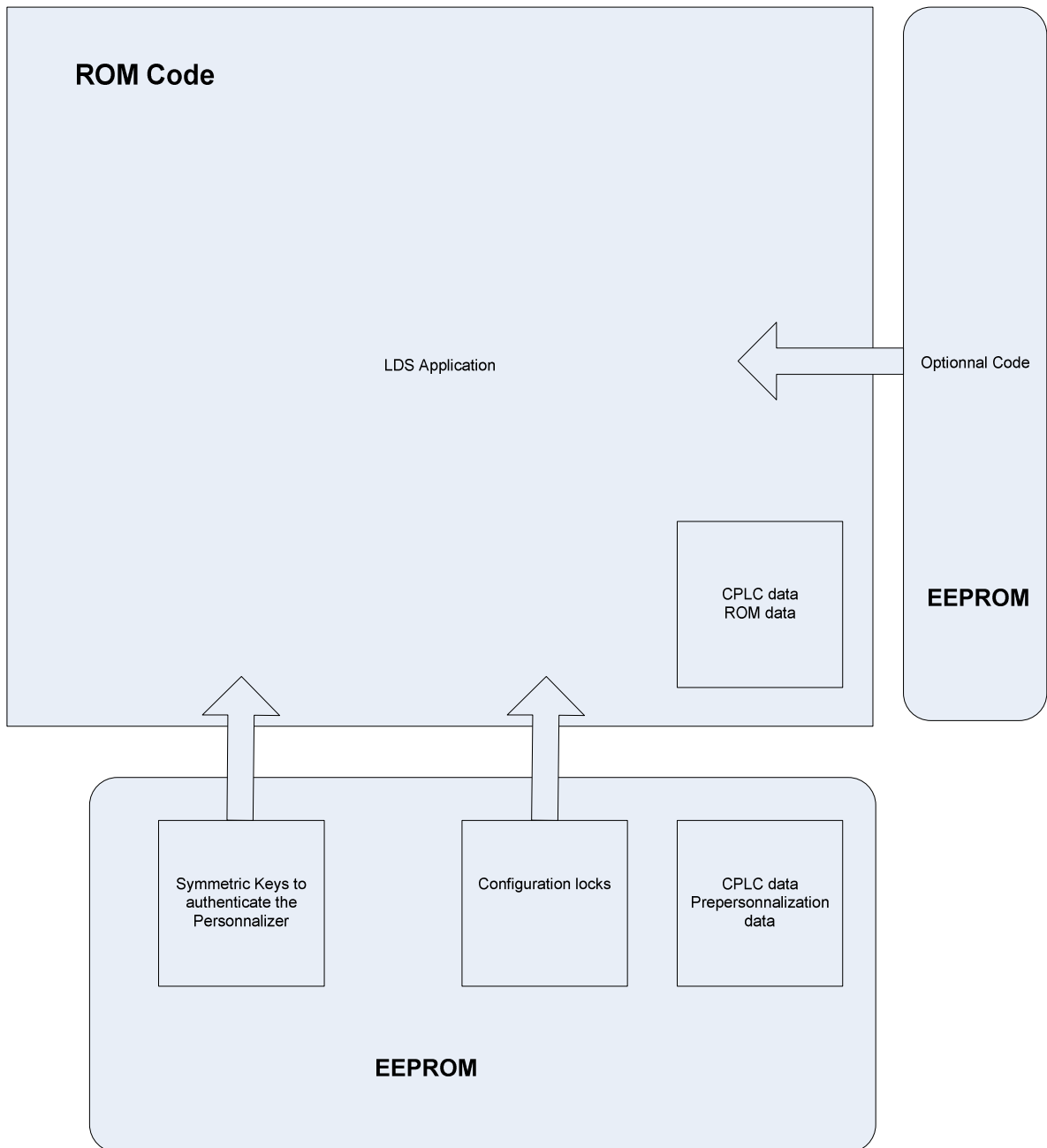
Even though the optional code may be loaded in phase 5, it is important to notice the following issues:

- The optional code does not modify TSF
- The optional code can only be loaded by the manufacturer agent, having its personalisation keys

Therefore, the phase 5, when an optional code is loaded may be covered by AGD\_PRE.

|                       |                    |                          |              |
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Initialization of the TOE software



**Figure 5 : Initialization of the TOE software**

### 3.7 Summary of the production environment

In this chapter, the TOE life cycle envisioned is the one of PP 9911 ([R28])

Three situations are envisioned for the production environments of the TOE.

- case 1: the optional code is loaded in phase 3 by the IC manufacturer. All the procedure is covered by its certificates. In phase 4 and 5, only the pre-personalisation of the TOE is performed as described in §3.6.2.2.
- case 2: the optional code is loaded in phase 5 in a manufacturing site of **OBERTHUR TECHNOLOGIES**
- case 3: the optional code is loaded in phase 5 in a manufacturing site of a **contractor**

This is summarized in the following table:

| TOE life cycle | Case 1   |   | Case 2   |  |
|----------------|--|---|--|--|
|                | Phases   | Environment   | Phases   | Environment                              |
| 3              | Optional code loading  | Production environment at NXP                                     | N/A  | Production environment at NXP            |
| 4              | IC packaging   | Production environment of a (contractor or OBERTHUR TECHNOLOGIES) | IC packaging   | OBERTHUR TECHNOLOGIES manufacturing site |
| 5              | Set up of the TOE  |   | Optional code loading<br>Set up of the TOE   |  |
| 6              | Personalisation of the MRTD (While the TOE is under the Personalisation Agent's operation) | Production environment of the customer                            | Personalisation of the MRTD (While the TOE is under the Personalisation Agent's operation) | Production environment of the customer   |
| 7              | Operational Use  |   | Operational Use  |  |

**Table 1 : Production environments of the TOE – case 1 & 2**

| TOE life cycle | Case 3   |  |
|----------------|--|--|
|                | Phases   | Environment                            |
| 3              | N/A  | Production environment at NXP          |
| 4              | IC packaging   | contractor manufacturing site          |
| 5              | Optional code loading<br>Set up of the TOE   |  |
| 6              | Personalisation of the MRTD (While the TOE is under the Personalisation Agent's operation) | Production environment of the customer |
| 7              | Operational Use  |  |

**Table 2 : Production environments of the TOE – case 3**

### 3.7.1 User environment

#### 3.7.1.1 TOE Personalisation and testing (phase 6)

At the end of the phase 5, the card manufacturer delivers the TOE to the personaliser of the MRTD device.

The TOE delivered to the personalizer has the following features:

- The personalizer must authenticate itself prior to any data exchange with the TOE. This authentication is performed by a cryptographic mean based on triple DES algorithm
- The TOE can be identified by the retrieval of its CPLC data.
- All the system files are created (key files, application data,...), as well as the EF.CVCA
- The optional code is loaded and is used by the TOE.
- The file EF.TOE\_Identification is created and initialized. It is up to the personaliser that receives the TOE to perform the following steps:

The personalisation agent is responsible of:

- creating the DGs it needs
- loading the data into the DGs
- Setting the lock to enable the active authentication feature (if needed), and the BAC
- loading the key(s) – Chip authentication keys, CVCA keys, BAC keys, Active authentication keys (if activated)
- Optionally generating the key for Active Authentication if not loaded
- Loading the CVCA certificate
- Loading the counter limit for the BAC authentication and the Terminal authentication.
- Updating the CPLC data to fill the personalisation data
- Setting the lock to block the CPLC data retrieval in free mode. This feature ensure the CPLC data can not be read without any BAC authentication
- Setting the lock to indicate the TOE is personalized: the TOE switches in used phase.

Once the personalisation agent finished the electrical personalisation, it TOE is switched into personalized phase. This transition is irreversible.

**Note:**

First of all, Two Chip authentication key is available (RSA and Elliptic curve). Moreover, the CVCA root key(s) does not need to be of the same type and length as the chip authentication key.

E.g., if the chip authentication key is a private key over an elliptic curve of 256 bits, the root CVCA key(s) can be a public key over 2048 bits RSA.

#### 3.7.1.2 TOE Operationnal Use (phase 7)

The TOE is delivered to the holder of the passport. The TOE behaves as described in [R1], [R2], [R4].

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### 3.8 Description of the TOE's scope

The scope of this present security target is:

- TOE development phase realized in the OBERTHUR TECHNOLOGIES environment in phase 1
- TOE manufacturing phase realized in the NXP environment in phase 2 & 3

All other phases are out of the scope of the TOE. (i.e. security assurance requirements for the corresponding environment are out of the scope).

The TOE embedded software, developed and embedded during phases 1 to 3, aims to control and protect the TOE during phases 4 to 7.

As such, this Security Target addresses all the security features put in place in phases 4 to 7 but that are developed in phase 1 [R10], [R11] and [R12] addresses the security requirements for phases 2 and 3 for the same objective.

**Remark:**

According to [R9] the phase 4 related to the embedding of the chip in a booklet and the connection of the antenna has been removed from the TOE scope.

### 3.9 Mapping of the TOE life cycle with the life cycle described in the Protection Profile

The protection profile considered considers a life cycle slightly different from the one depicted above. Here we provide a mapping between the PP we are considering and [R28]

| TOE life cycle | Matching life cycle as described in the PP   |
|----------------|--|
| 1              | Phase 1 : development  |
| 2              |  |
| 3              | Phase 2 : Manufacturing  |
| 4              |  |
| 5              |  |
| 6              | Phase 3 : Personalisation of the MRTD (While the TOE is under the Personalisation Agent's operation) |
| 7              | Phase 4 :Operational Use   |

**Table 3 : Mapping of life cycle states**

For more details about this mapping, see [R29].

## 4 TOE Security Environment

### 4.1 Assets

The assets to be protected by the TOE include the User Data on the MRTD's chip.

#### Logical MRTD Data

The logical MRTD data consists of the EF.COM, EF.DG1 to EF.DG16 (with different security needs) and the Document Security Object EF.SOD according to LDS [R3]. These data are user data of the TOE. The EF.COM lists the existing elementary files (EF) with the user data. The EF.DG1 to EF.DG13 and EF.DG16 contain personal data of the MRTD holder. The Chip Authentication Public Key (EF.DG14) is used by the inspection system for the Chip Authentication.

The Active Authentication Public Key Info in DG 15 is used by the inspection system for Active Authentication of the chip. The Document security object is used by the inspection system for Passive Authentication of the logical MRTD.

The TOE, contains two sets of identification data

- A set uniquely identifying the chip, usually called the CPLC data.
- A set enabling to identify the TOE, in particular, its PP evaluation

The behaviour of the TOE securely handles its internal state, so that it can

- distinguish between the "Phase 3 - Personalization of the MRTD" and the "Phase 4 – Operational Phase". It is ensured by its life cycle state.
- Ensure the TOE can not be erased
- Ensure no tearing can arises
- The configuration chosen (BAC, AA, EAC, Get Data is forbidden)

While a session is established with an inspection system, the TOE handles the two session keys used to ensure the confidentiality and integrity of the communications.

To ensure the TOE is protected against brute force attacks, both the BAC protocol and the Terminal authentication are protected by a counter error, distinct for each authentication, increased at each wrong consecutive authentication. When the limit is exceeded, the TOE performs the authentication within a period of time constantly increasing. These counters are reset when the matching authentication is successfully performed

All these data may be sorted out in two different categories.

- If they are specific to the user, they are User data
- If they ensures the correct behaviour of the application, they are TOS Security Data



| User Data  |   |
|--|---|
| <b>CPLC Data</b>   | Data uniquely identifying the chip. They are considered as user data as they enable to track the holder.  |
| <b>Personal Data of the MRTD holder (EF.DG1, EF.DG2, EF.DG5 – EF.DG13,EF.DG16)</b> | Contains identification data of the holder  |
| <b>Sensitive biometric reference data (EF.DG3, EF.DG4)</b>                         | Contain the fingerprint and the iris picture  |
| <b>Document Security Object (SOD) in EF.SOD</b>                                    | Contain a certificate ensuring the integrity of the file stored within the MRTD and their authenticity. It ensures the data are issued by a genuine country |
| <b>Common data in EF.COM</b>   | Declare the data the travel document contains   |
| <b>Active Authentication Public Key in EF.DG15</b>                                 | Contain public data enabling to authenticate the chip thanks to an active authentication  |
| <b>Chip Authentication Public Key in EF.DG14</b>                                   | Contain public data enabling to authenticate the chip thanks to a chip authentication   |

Table 4 : User Data

| TSF Data   |  |
|--|--|
| <b>TOE_ID</b>  | Data enabling to identify the TOE  |
| <b>Personalisation Agent reference authentication Data</b> | Private key enabling to authenticate the Personalisation agent   |
| <b>Basic Access Control (AC) Key</b>                       | Master keys used to establish a trusted channel between the Basic Inspection Terminal and the travel document                              |
| <b>Active Authentication private key</b>                   | Private key the chip uses to perform an active authentication  |
| <b>Session keys for the secure channel</b>                 | Session keys used to protect the communication in confidentiality and in integrity   |
| <b>Life Cycle State</b>                                    | Life Cycle state of the TOE  |
| <b>Error counter for BAC protocol</b>                      | Counter increased at each wrong consecutive authentication number of wrong consecutive authentication for BAC protocol                     |
| <b>Public Key CVCA</b>                                     | Trust point of the travel document stored in persistent memory   |
| <b>CVCA Certificate</b>                                    | All the data related to the CVCA key (expiration date, name, ..) stored in persistent memory   |
| <b>Current Date</b>  | Current date of the travel document  |
| <b>Chip Authentication private Key</b>                     | Private key the chip uses to perform a chip authentication   |
| <b>Error counter for Terminal protocol</b>                 | Counter increased at each wrong consecutive authentication number of wrong consecutive authentication for terminal authentication protocol |

Table 5 : TSF Data

An additional asset is the following more general one.

|                       |                    |                          |              |
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## Authenticity of the MRTD's chip

The authenticity of the MRTD's chip personalized by the issuing State or Organization for the MRTD's holder is used by the traveller to authenticate himself as possessing a genuine MRTD.

### 4.2 Subjects

This security target considers the following subjects:

| Subject  | Definition   |
|--|--|
| <b>Manufacturer</b>                              | The generic term for the IC Manufacturer producing the integrated circuit and the MRTD Manufacturer completing the IC to the MRTD's chip. The Manufacturer is the default user of the TOE during the Phase 2 Manufacturing. The TOE does not distinguish between the users IC Manufacturer and MRTD Manufacturer using this role Manufacturer  |
| <b>MRTD Holder</b>                               | The rightful holder of the MRTD for whom the issuing State or Organization personalised the MRTD   |
| <b>Traveller</b>                                 | Person presenting the MRTD to the inspection system and claiming the identity of the MRTD holder   |
| <b>Personalization Agent</b>                     | The agent is acting on the behalf of the issuing State or Organisation to personalize the MRTD for the holder by some or all of the following activities <ul style="list-style-type: none"> <li>(i) establishing the identity the holder for the biographic data in the MRTD,</li> <li>(iii) enrolling the biometric reference data of the MRTD holder i.e. the portrait, the encoded finger image(s) and/or the encoded iris image(s)</li> <li>(iv) writing these data on the physical and logical MRTD for the holder as defined for global, international and national interoperability and</li> <li>(v) signing the Document Security Object defined in [R3].</li> </ul> |
| <b>Country Verifying Certification Authority</b> | The Country Verifying Certification Authority (CVCA) enforces the privacy policy of the issuing Country or Organization with respect to the protection of sensitive biometric reference data stored in the MRTD. The CVCA represents the country specific root of the PKI of Inspection Systems and creates the Document Verifier Certificates within this PKI. The updates of the public key of the CVCA are distributed in form of Country Verifying CA Link-Certificates.   |
| <b>Document Verifier</b>                         | The Document Verifier (DV) enforces the privacy policy of the receiving Country with respect to the protection of sensitive biometric reference data to be handled by the Extended Inspection Systems. The Document Verifier manages the authorization of the Extended Inspection Systems for the sensitive data of the MRTD in the limits provided by the issuing States or Organizations in form of the Document Verifier Certificates.  |
| <b>Inspection system</b>                         | A technical system used by the border control officer of the receiving State (i) examining an MRTD presented by the traveller and verifying its authenticity and (ii) verifying the traveller as MRTD holder.<br>The <b>Basic Inspection System (BIS)</b>  |

|                 |   |
|-----------------|---|
|                 | <ul style="list-style-type: none"> <li>(i) contains a terminal for the contact less communication with the MRTD's chip,</li> <li>(ii) implements the terminals part of the Basic Access Control Mechanism and</li> <li>(iii) gets the authorization to read the logical MRTD under the Basic Access Control by optical reading the printed data in the MRZ or other parts of the passport book providing this information.</li> </ul> <p>The <b>General Inspection System (GIS)</b> is a <b>Basic Inspection System</b> which implements additional the Chip Authentication Mechanism.</p> <p>The <b>Extended Inspection System (EIS)</b> in addition to the <b>General Inspection System</b></p> <ul style="list-style-type: none"> <li>(i) implements the Terminal Authentication Protocol and</li> <li>(ii) is authorized by the issuing State or Organization through the Document Verifier of the receiving State to read the sensitive biometric reference data. The security attributes of the EIS are defined of the Inspection System Certificates.</li> <li>(iii) implements the Active Authentication Mechanism</li> </ul> |
| <b>Terminal</b> | A terminal is any technical system communicating with the TOE through the contact less interface  |
| <b>Attacker</b> | <p>A threat agent trying</p> <ul style="list-style-type: none"> <li>(i) to identify and to trace the movement the MRTD's chip remotely (i.e. without knowing or reading the printed MRZ data),</li> <li>(ii) to read or to manipulate the logical MRTD without authorization, or</li> <li>(iii) to forge a genuine MRTD</li> </ul>  |

**Table 6 : Subjects**

### **Application note 6 on Inspection system**

According to [R3] the support of

- (i) the Passive Authentication mechanism is mandatory, and
- (ii) the Basic Access Control is optional.

In the context of this protection profile the **Primary Inspection System** does not implement the terminal part of the Basic Access Control. It is therefore not able to read the logical MRTD because the logical MRTD of the TOE is protected by Basic Access Control. Therefore this protection profile will not consider the use of **Primary Inspection System** by the receiving State or Organization. The TOE of the current protection profile does not allow the Personalization agent to disable the Basic Access Control for use with **Primary Inspection Systems** as described in the BSI-PP-0017 Machine Readable Travel Document with „ICAO Application“, Basic Access Control.

### **Application note 7 on Attacker**

An impostor is attacking the inspection system as TOE IT environment independent on using a genuine, counterfeit or forged MRTD. Therefore the impostor may use results of successful attacks against the TOE but his or her attack itself is not relevant for the TOE.

|                       |                    |                          |              |
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### 4.3 Assumptions

The assumptions describe the security aspects of the environment in which the TOE will be used or is intended to be used.

|                     |   |
|---------------------|---|
| <b>A.Pers_Agent</b> | <b>Personalization of the MRTD's chip</b> |
|---------------------|---|

The Personalization Agent ensures the correctness of

- (i) the logical MRTD with respect to the MRTD holder,
- (ii) the Document Basic Access Keys,
- (iii) the Active Authentication Public Key Info (DG15) if stored on the MRTD's chip,
- (iv) the Chip Authentication Public Key (EF.DG14) if stored on the MRTD's chip, and
- (v) the Document Signer Public Key Certificate (if stored on the MRTD's chip).

The Personalization Agent signs the Document Security Object. The Personalization Agent bears the Personalization Agent Authentication to authenticate himself to the TOE by symmetric cryptographic mechanisms.

|                   |   |
|-------------------|---|
| <b>A.Insp_Sys</b> | <b>Inspection Systems for global interoperability</b> |
|-------------------|---|

The Inspection System is used by the border control officer of the receiving State

- (i) examining an MRTD presented by the traveller and verifying its authenticity and
- (ii) verifying the traveller as MRTD holder.

The Basic Inspection System for global interoperability

- (i) includes the Country Signing Public Key and the Document Signer Public Key of each issuing State or Organization, and
- (ii) implements the terminal part of the Basic Access Control [R1]

The **Basic Inspection System** reads the logical MRTD being under Basic Access Control and performs the Passive Authentication to verify the logical MRTD.

The **General Inspection System** in addition to the Basic Inspection System implements the Chip Authentication Mechanism.

The **General Inspection System** verifies the authenticity of the MRTD's chip during inspection and establishes secure messaging with keys established by the Chip Authentication Mechanism.

The **Extended Inspection System** in addition to the General Inspection System

- (i) supports the Terminal Authentication Protocol and
- (ii) is authorized by the issuing State or Organization through the Document Verifier of the receiving State to read the sensitive biometric reference data.

The Active authentication is also optional and can be enabled or disabled by the Personalization agent.

|                        |                                       |
|------------------------|---------------------------------------|
| <b>A.Signature_PKI</b> | <b>PKI for Passive Authentication</b> |
|------------------------|---------------------------------------|

The issuing and receiving States or Organisations establish a public key infrastructure for passive authentication i.e. digital signature creation and verification for the logical MRTD. The issuing State or Organization runs a Certification Authority (CA) which

- (i) securely generates, stores and uses the Country Signing CA Key pair, and
- (ii) manages the MRTD's Chip Authentication Key Pairs. The CA keeps the Country Signing CA Private Key secret and distributes the Country Signing CA Public Key to ICAO, all receiving States maintaining its integrity.

The Document Signer

- (i) generates the Document Signer Key Pair,

- (ii) hands over the Document Signer Public Key to the CA for certification,
- (iii) keeps the Document Signer Private Key secret and
- (iv) uses securely the Document Signer Private Key for signing the Document Security Objects of the MRTDs.

The CA creates the Document Signer Certificates for the Document Signer Public Keys and distributes them to the receiving States and organizations.

|                   |                                   |
|-------------------|-----------------------------------|
| <b>A.Auth_PKI</b> | <b>PKI for Inspection Systems</b> |
|-------------------|-----------------------------------|

The issuing and receiving States or Organisations establish a public key infrastructure for card verifiable certificates of the extended access control. The Country Verifying Certification Authorities, the Document Verifier and Extended Inspection Systems hold authentication key pairs and certificates for their public keys encoding the access control rights. The Country Verifying Certification Authorities of the issuing States or Organisations are signing the certificates of the Document Verifier and the Document Verifiers are signing the certificates of the Extended Inspection Systems of the receiving States or Organisations. The issuing States or Organizations distributes the public key of their Country Verifying Certification Authority to their MRTD's chip.

#### 4.4 Organisational Security Policies

The TOE shall comply to the following organisation security policies (OSP) as security rules, procedures, practices, or guidelines imposed by an organisation upon its operations (see CC part 1, sec. 3.2).

|                   |   |
|-------------------|---|
| <b>P.Manufact</b> | <b>Manufacturing of the MRTD's chip</b> |
|-------------------|---|

The IC Manufacturer and MRTD Manufacturer ensure the quality and the security of the manufacturing process and control the MRTD's material in the Phase 2 Manufacturing. The Initialisation Data are written by the IC Manufacturer to identify the IC uniquely. The MRTD Manufacturer writes the Pre-personalization Data which contains at least the Personalization Agent Key.

|  |  |
|--|--|
| <b>P.Personalization Organization only</b> | <b>Personalization of the MRTD by issuing State or Organization only</b> |
|--|--|

The issuing State or Organization guarantees the correctness of the biographical data, the printed portrait and the digitised portrait, the biometric reference data and other data of the logical MRTD with respect to the MRTD holder. The personalization of the MRTD for the holder is performed by authorized agents of the issuing State or Organization only.

|                        |  |
|------------------------|--|
| <b>P.Personal_Data</b> | <b>Personal data protection policy</b> |
|------------------------|--|

The biographical data and their summary printed in the MRZ and stored on the MRTD's chip (EF.DG1), the printed portrait and the digitised portrait (EF.DG2), the biometric reference data of finger(s) (EF.DG3), the biometric reference data of iris image(s) (EF.DG4) and data according to LDS (EF.DG5 to EF.DG13, EF.DG16) stored on the MRTD's chip are personal data of the MRTD holder. These data groups are intended to be used only with agreement of the MRTD holder i.e. if the MRTD is presented to an inspection system. Additional to the Basic Access Control Authentication defined by ICAO in [R1] the MRTD's chip shall protect the confidentiality and integrity of the personal data during transmission to the General Inspection System after Chip authentication.

#### Application note 8:

|                       |                    |                          |              |
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The organisational security policy **P.Personal\_Data** is drawn from the ICAO Technical Report [R1]. Note, that the Document Basic Access Key is defined by the TOE environment and loaded to the TOE by the Personalization Agent.

|                         |  |
|-------------------------|--|
| <b>P.Sensitive_Data</b> | <b>Privacy of sensitive biometric reference data</b> |
|-------------------------|--|

The biometric reference data of finger(s) (EF.DG3) and iris image(s) (EF.DG4) are sensitive private personal data of the MRTD holder. The sensitive biometric reference data can be used only by inspection systems which are authorized for this access at the time the MRTD is presented to the inspection system. The issuing State or Organization authorizes the Document Verifiers of the receiving States to manage the authorization of inspection systems within the limits defined by the Document Verifier Certificate.

#### 4.5 Specific Organisational Security Policies

|                    |  |
|--------------------|--|
| <b>P.Plat-AppI</b> | <b>Development according to the IC recommandations</b> |
|--------------------|--|

The development of the Composite TOE was lead in accordance with the recommendations issued by the IC manufacturer. For More details see the “Design Compliance evidences”.

|                                    |                                     |
|------------------------------------|-------------------------------------|
| <b>P.Sensitive_Data_Protection</b> | <b>Protection of sensitive data</b> |
|------------------------------------|-------------------------------------|

All the sensitive data are at least protected in integrity. The keys are protected in both integrity and confidentiality.

|                                    |  |
|------------------------------------|--|
| <b>P.Key_Function<br/>the keys</b> | <b>Design of the cryptographic routines in order to protect the keys</b> |
|------------------------------------|--|

All the cryptographic routines are designed in such a way that they are protected against probing and do not cause any information leakage that may be used by an attacker.

#### 4.6 Threats

This section describes the threats to be averted by the TOE independently or in collaboration with its IT environment. These threats result from the TOE method of use in the operational environment and the assets stored in or protected by the TOE.

The TOE in collaboration with its IT environment shall avert the threats as specified below.

|                  |                                      |
|------------------|--------------------------------------|
| <b>T.Chip_ID</b> | <b>Identification of MRTD’s chip</b> |
|------------------|--------------------------------------|

An attacker trying to trace the movement of the MRTD by identifying remotely the MRTD’s chip by establishing or listening a communication through the contactless communication interface. The attacker can not read optically and does not know in advance the physical MRTD.

|                   |                                  |
|-------------------|----------------------------------|
| <b>T.Skimming</b> | <b>Skimming the logical MRTD</b> |
|-------------------|----------------------------------|

An attacker imitates the inspection system to read the logical MRTD or parts of it via the contactless communication channel of the TOE. The attacker can not read and does not know in advance the physical MRTD.

|                              |  |
|------------------------------|--|
| <b>T.Read_Sensitive_Data</b> | <b>Read the sensitive biometric reference data</b> |
|------------------------------|--|

An attacker with high attack potential knowing the Document Basic Access Keys is trying to gain the sensitive biometric reference data through the communication interface of the MRTD's chip.

The attack **T.Read Sensitive Data** is similar to the threats **T.Skimming** in respect of the attack path (communication interface) and the motivation (to get data stored on the MRTD's chip) but differs from those in the asset under the attack (sensitive biometric reference data vs. digital MRZ, digitized portrait and other data), the opportunity (i.e. knowing Document Basic Access Keys) and therefore the possible attack methods. Note, that the sensitive biometric reference data are stored only on the MRTD's chip as private sensitive personal data whereas the MRZ data and the portrait are visual readable on the physical MRTD as well.

|                  |                                       |
|------------------|---------------------------------------|
| <b>T.Forgery</b> | <b>Forgery of data on MRTD's chip</b> |
|------------------|---------------------------------------|

An attacker alters fraudulently the complete stored logical MRTD or any part of it including its security related data in order to impose on an inspection system by means of the changed MRTD holder's identity or biometric reference data.

This threat comprises several attack scenarios of MRTD forgery. The attacker may alter the biographical data on the biographical data page of the passport book, in the printed MRZ and in the digital MRZ to claim another identity of the traveller. The attacker may alter the printed portrait and the digitized portrait to overcome the visual inspection of the inspection officer and the automated biometric authentication mechanism by face recognition. The attacker may alter the biometric reference data to defeat automated biometric authentication mechanism of the inspection system. The attacker may combine data groups of different logical MRTDs to create a new forged MRTD, e.g. the attacker write the digitized portrait and optional biometric reference data of finger read from the logical MRTD of a traveller into an other MRTD's chip leaving their digital MRZ unchanged to claim the identity of the holder this MRTD. The attacker may also copy the complete unchanged logical MRTD in another contactless chip.

|                                  |
|----------------------------------|
| <b>T.Counterfeit MRTD's chip</b> |
|----------------------------------|

An attacker with high attack potential produces an unauthorized copy or reproduction of a genuine MRTD's chip to be used as part of a counterfeit MRTD. This violates the authenticity of the MRTD's chip used for authentication of a traveller by possession of a MRTD.

The attacker may generate a new data set or extract completely or partially the data from a genuine MRTD's chip and copy them on another appropriate chip to imitate this genuine MRTD's chip.

The TOE shall avert the threat as specified below.

|                     |                               |
|---------------------|-------------------------------|
| <b>T.Abuse-Func</b> | <b>Abuse of Functionality</b> |
|---------------------|-------------------------------|

An attacker may use functions of the TOE which shall not be used in TOE operational phase in order

- (i) to manipulate User Data,
- (ii) to manipulate (explore, bypass, deactivate or change) security features or functions of the TOE or
- (iii) to disclose or to manipulate TSF Data.

This threat addresses the misuse of the functions for the initialisation and the personalization in the operational state after delivery to MRTD holder.

|                              |   |
|------------------------------|---|
| <b>T.Information_Leakage</b> | <b>Information Leakage from MRTD's chip</b> |
|------------------------------|---|

An attacker may exploit information that is leaked from the TOE during its usage in order to disclose confidential TSF data. The information leakage may be inherent in the normal operation or caused by the attacker.

Leakage may occur through emanations, variations in power consumption, I/O characteristics, clock frequency, or by changes in processing time requirements. This leakage may be interpreted as a covert channel transmission but is more closely related to measurement of operating parameters, which may be derived either from measurements of the contactless interface (emanation) or direct measurements (by contact to the chip still available even for a contactless chip) and can then be related to the specific operation being performed. Examples are the Differential Electromagnetic Analysis (DEMA) and the Differential Power Analysis (DPA). Moreover the attacker may try actively to enforce information leakage by fault injection (e.g. Differential Fault Analysis).

|                      |                           |
|----------------------|---------------------------|
| <b>T.Phys-Tamper</b> | <b>Physical Tampering</b> |
|----------------------|---------------------------|

An attacker may perform physical probing of the MRTD's chip in order

- (i) to disclose TSF Data, or
- (ii) to disclose/reconstruct the MRTD's chip Embedded Software.

An attacker may physically modify the MRTD's chip in order to

- (i) modify security features or functions of the MRTD's chip,
- (ii) modify security functionalities of the MRTD's chip Embedded Software,
- (iii) modify User Data or
- (iv) (iv) to modify TSF data.

The physical tampering may be focused directly on the discloser or manipulation of TOE User Data (e.g. the biometric reference data for the inspection system) or TSF Data (e.g. Authentication key of the MRTD's chip) or indirectly by preparation of the TOE to following attack methods by modification of security features (e.g. to enable information leakage through power analysis).

Physical tampering requires direct interaction with the MRTD's chip internals. Techniques commonly employed in IC failure analysis and IC reverse engineering efforts may be used. Before that, the hardware security mechanisms and layout characteristics need to be identified. Determination of software design including treatment of User Data and TSF Data may also be a pre-requisite. The modification may result in the deactivation of a security function. Changes of circuitry or data can be permanent or temporary.

|                      |  |
|----------------------|--|
| <b>T.Malfunction</b> | <b>Malfunction due to Environmental Stress</b> |
|----------------------|--|

An attacker may cause a malfunction of TSF or of the MRTD's chip Embedded Software by applying environmental stress in order to

- (i) deactivate or modify security features or functions of the TOE or
- (ii) circumvent, deactivate or modify security functionalities of the MRTD's chip Embedded Software.

This may be achieved e.g. by operating the MRTD's chip outside the normal operating conditions, exploiting errors in the MRTD's chip Embedded Software or misusing administration function. To exploit these vulnerabilities an attacker needs information about the functional operation.



## 5 Security Objectives

This chapter describes the security objectives for the TOE and the security objectives for the TOE environment. The security objectives for the TOE environment are separated into security objectives for the development and production environment and security objectives for the operational environment.

### 5.1 Security Objectives for the TOE

This section describes the security objectives for the TOE addressing the aspects of identified threats to be countered by the TOE and organisational security policies to be met by the TOE.

|                   |   |
|-------------------|---|
| <b>OT.AC_Pers</b> | <b>Access Control for Personalization of logical MRTD</b> |
|-------------------|---|

The TOE must ensure that the logical MRTD data groups DG1 to DG16, the Document security object according to LDS [R3] and the TSF data can be written by authorized Personalization Agents. The logical MRTD data groups DG1 to DG16 and the TSF data can be written only once and can not be changed after personalization. The Document security object can be updated by authorized Personalization Agents if data in the data groups DG 3 to DG16 are added.

Only the Personalization Agent shall be allowed to enable or to disable the TSF Basic Access Control.

#### Application note 9:

The **OT.AC\_Pers** implies that:

1. The data of the LDS groups written during personalization for MRTD holder (at least DG1 and DG2) can not be changed by write access after personalization,
2. The Personalization Agents may
  - (i) add (fill) data into the LDS data groups not written yet, and
  - (ii) update and sign the Document Security Object accordantly.

|                    |                                   |
|--------------------|-----------------------------------|
| <b>OT.Data_Int</b> | <b>Integrity of personal data</b> |
|--------------------|-----------------------------------|

The TOE must ensure the integrity of the logical MRTD stored on the MRTD's chip against physical manipulation and unauthorized writing. The TOE must ensure the integrity of the logical MRTD data during their transmission to the General Inspection System after Chip Authentication.

|                     |   |
|---------------------|---|
| <b>OT.Data_Conf</b> | <b>Confidentiality of personal data</b> |
|---------------------|---|

The TOE must ensure the confidentiality of the data in EF.DG1, EF.DG2 and EF.DG5 to EF.DG16 and the Document Security Object of the logical MRTD by granting read access to terminals successfully authenticated by as

- (i) Personalization Agent or
- (ii) Basic Inspection System or
- (iii) Extended Inspection System.

The TOE implements the Basic Access Control as defined by ICAO [R1] and enforce **Basic Inspection System** to authenticate itself by means of the Basic Access Control based on knowledge of the Document Basic Access Key. The TOE must ensure the confidentiality of the logical MRTD data during their transmission to the **General Inspection System** after Chip Authentication.

**Application note 10:**

The traveller grants the authorization for reading the personal data in EF.DG1 to EF.DG16 to the inspection system by presenting the MRTD. The MRTD's chip shall provide read access to these data for terminals successfully authenticated by means of the Basic Access Control based on knowledge of the Document Basic Access Keys. The security objective **OT.Data\_Conf** requires the TOE to ensure the strength of the security functionality Basic Access Control Authentication independent on the quality of the Document Basic Access Keys which is defined by the TOE environment and loaded into the TOE by the Personalization Agent. Any attack based on decision of the ICAO Technical Report [R3] that the inspection system derives Document Basic Access Keys from the printed MRZ data does not violate the security objective **OT.Data\_Conf**

|  |  |
|--|--|
| <b>OT.Sens_Data_Conf</b>   | <b>Confidentiality of sensitive biometric reference data</b> |
| <p>The TOE must ensure the confidentiality of the sensitive biometric reference data (EF.DG3 and EF.DG4) by granting read access only to authorized inspection systems. The authorization of the inspection system is drawn from the Inspection System Certificate used for the successful authentication and shall be a non-strict subset of the authorization defined in the Document Verifier Certificate in the certificate chain to the Country Verifier Certification Authority of the issuing State or Organization. The TOE must ensure the confidentiality of the logical MRTD data during their transmission to the Extended Inspection System. The confidentiality of the sensitive biometric reference data shall be protected against attacks with high attack potential.</p> |  |

|   |   |
|---|---|
| <b>OT.Identification</b>  | <b>Identification and Authentication of the TOE</b> |
| <p>The TOE must provide means to store IC Identification Data in its non-volatile memory. The IC Identification Data must provide a unique identification of the IC during Phase 2 "Manufacturing" and Phase 3 "Personalization of the MRTD". If the TOE is configured for use with Basic Inspection Terminals only in Phase 4 "Operational Use" the TOE shall identify themselves only to a successful authenticated Basic Inspection System or Personalization Agent.</p> |   |

**Application note 11:**

The TOE security objective **OT.Identification** addresses security features of the TOE to support the life cycle security in the manufacturing and personalization phases. The IC Identification Data are used for TOE identification in Phase 2 "Manufacturing" and for traceability and/or to secure shipment of the TOE from Phase 2 "Manufacturing" into the Phase 3 "Personalization of the MRTD". The **OT.Identification** addresses security features of the TOE to be used by the TOE manufacturing environment as described in its security objective **OD.Material**. In the Phase 4 "Operational Use" the TOE is identified by the passport number as part of the printed and digital MRZ. The **OT.Identification** forbids the output of any other IC (e.g. integrated circuit serial number ICCSN) or a MRTD identifier through the contactless interface before successful authentication as Basic Inspection System or as Personalization Agent

|  |  |
|--|--|
| <b>OT.Chip_Auth_Proof</b>  | <b>Proof of MRTD'S chip authenticity</b> |
| <p>The TOE must support the General Inspection Systems to verify the identity and authenticity of the MRTD's chip as issued by the identified issuing State or Organization by means of the Chip Authentication as defined in [R4] The authenticity prove provided by MRTD's chip shall be protected against attacks with high attack potential.</p> |  |

**Application note 12:**

The **OT.Chip\_Auth\_Proof** implies the MRTD's chip to have

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- (i) a unique identity as given by the MRTD's Document number,
- (ii) a secret to prove its identity by knowledge i.e. a private authentication key as TSF data.  
The TOE shall protect this TSF data to prevent their misuse.

The terminal shall have the reference data to verify the authentication attempt of MRTD's chip i.e. a certificate for the Chip Authentication Public Key that fit to the Chip Authentication Private Key of the MRTD's chip. This certificate is provided by

- (i) the Chip Authentication Public Key (EF.DG14) in the LDS [R3] and
- (ii) the hash value of the Authentication Public Key in the Document Security Object signed by the Document Signer.

|                           |  |
|---------------------------|--|
| <b>OT.Prot_Abuse-Func</b> | <b>Protection against Abuse of Functionality</b> |
|---------------------------|--|

The TOE must prevent that functions of the TOE which may not be used after TOE Delivery can be abused in order

- (i) to disclose critical User Data,
- (ii) to manipulate critical User Data of the Smart card Embedded Software,
- (iii) to manipulate Soft-coded Smart card Embedded Software or
- (iv) bypass, deactivate, change or explore security features or functions of the TOE.

Details of the relevant attack scenarios depend, for instance, on the capabilities of the Test Features provided by the IC Dedicated Test Software which are not specified here.

The following TOE security objectives address the protection provided by the MRTD's chip independent on the TOE environment.

|                         |   |
|-------------------------|---|
| <b>OT.Prot_Inf_Leak</b> | <b>Protection against Information Leakage</b> |
|-------------------------|---|

The TOE must provide protection against disclosure of confidential TSF data stored and/or processed in the MRTD's chip by:

- (i) Measurement and analysis of the shape and amplitude of signals or the time between events found by measuring signals on the electromagnetic field, power consumption, clock, or I/O lines and
- (ii) Forcing a malfunction of the TOE and/or
- (iii) A physical manipulation of the TOE.

**Application note 13:**

This objective pertains to measurements with subsequent complex signal processing due to normal operation of the TOE or operations enforced by an attacker. Details correspond to an analysis of attack scenarios which is not given here.

|                            |  |
|----------------------------|--|
| <b>OT.Prot_Phys-Tamper</b> | <b>Protection against Physical Tampering</b> |
|----------------------------|--|

The TOE must provide protection the confidentiality and integrity of the User Data, the TSF Data, and the MRTD's chip Embedded Software. This includes protection against attacks with high attack potential by means of

- (i) Measuring through galvanic contacts which is direct physical probing on the chips surface except on pads being bonded (using standard tools for measuring voltage and current) or
- (ii) Measuring not using galvanic contacts but other types of physical interaction between charges (using tools used in solid-state physics research and IC failure analysis)
- (iii) Manipulation of the hardware and its security features, as well as
- (iv) Controlled manipulation of memory contents (User Data, TSF Data) with a prior
- (v) Reverse-engineering to understand the design and its properties and functions.

**Application note 14:**

In order to meet the security objectives **OT.Prot\_Phys-Tamper** the TOE must be designed and fabricated so that it requires a high combination of complex equipment, knowledge, skill, and time to be able to derive detailed design information or other information which could be used to compromise security through such a physical attack. This is addressed by the security objective **OD.Assurance**.

|                            |  |
|----------------------------|--|
| <b>OT.Prot_Malfunction</b> | <b>Protection against Malfunctions</b> |
|----------------------------|--|

The TOE must ensure its correct operation. The TOE must prevent its operation outside the normal operating conditions where reliability and secure operation has not been proven or tested. This is to prevent errors. The environmental conditions may include external energy (esp. electromagnetic) fields, voltage (on any contacts), clock frequency, or temperature.

**Application note 15:**

A malfunction of the TOE may also be caused using a direct interaction with elements on the chip surface. This is considered as being a manipulation (refer to the objective **OT.Prot\_Phys-Tamper**) provided that detailed knowledge about the TOE’s internals.

|                             |                                   |
|-----------------------------|-----------------------------------|
| <b>OT.Chip_Authenticity</b> | <b>Protection against forgery</b> |
|-----------------------------|-----------------------------------|

The TOE must support the Inspection Systems to verify the authenticity of the MRTD’s chip. The TOE stores a RSA or ECC private key to prove its identity, and that is used in chip authentication. This mechanism is described in [R1] as “Active Authentication”.

**5.2 Security Objectives for the environment**

*5.2.1 Security Objectives for the Development and Manufacturing Environment*

|   |   |
|---|---|
| <b>OD.Assurance<br/>Manufacturing Environment</b> | <b>Assurance Security Measures in Development and</b> |
|---|---|

The developer and manufacturer ensure that the TOE is designed and fabricated so that it requires a combination of complex equipment, knowledge, skill, and time to be able to derive detailed design information or other information which could be used to compromise security through attack. This includes the use of the Initialisation Data for unique identification of the TOE and the pre-personalization of the TOE including the writing of the Personalization Agent Authentication key(s). The developer provides necessary evaluation evidence that the TOE fulfils its security objectives and is resistant against obvious penetration attacks with high attack potential.

|                    |                                   |
|--------------------|-----------------------------------|
| <b>OD.Material</b> | <b>Control over MRTD Material</b> |
|--------------------|-----------------------------------|

The IC Manufacturer, the MRTD Manufacturer and the Personalization Agent must control all materials, equipment and information to produce, to initialise, to pre-personalize genuine MRTD materials and to personalize authentic MRTD in order to prevent counterfeit of MRTD using MRTD materials.

*5.2.2 Security Objectives for the Operational Environment*

|                                      |
|--------------------------------------|
| <b>Issuing State or Organization</b> |
|--------------------------------------|

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The Issuing State or Organization will implement the following security objectives of the TOE environment.

|                           |  |
|---------------------------|--|
| <b>OE.Personalization</b> | <b>Personalization of logical MRTD</b> |
|---------------------------|--|

The issuing State or Organization must ensure that the Personalization Agents acting on the behalf of the issuing State or Organisation

- (i) establish the correct identity of the holder and create biographic data for the MRTD,
- (ii) enroll the biometric reference data of the MRTD holder i.e. the portrait, the encoded finger image(s) and/or the encoded iris image(s) and
- (iii) personalize the MRTD for the holder together with the defined physical and logical security measures (including the digital signature in the Document Security Object) to protect the integrity and confidentiality of these data.

|                          |  |
|--------------------------|--|
| <b>OE.Pass_Auth_Sign</b> | <b>Authentication of logical MRTD by Signature</b> |
|--------------------------|--|

The Issuing State or Organization must

- (i) generate a cryptographic secure Country Signing Key Pair,
- (ii) ensure the secrecy of the Country Signing Private Key and sign Document Signer Certificates in a secure operational environment, and
- (iii) distribute the Certificate of the Country Signing Public Key to receiving States and organizations maintaining its authenticity and integrity.

The Issuing State or organization must

- (i) generate a cryptographic secure Document Signing Key Pair and ensure the secrecy of the Document Signer Private Keys,
- (ii) sign Document Security Objects of genuine MRTD in a secure operational environment only and
- (iii) distribute the Certificate of the Document Signing Public Key to receiving States and organizations.

The digital signature in the Document Security Object includes all data in the data groups DG1 to DG16 if stored in the LDS according to [R3].

|                         |                                |
|-------------------------|--------------------------------|
| <b>OE.Auth_Key_MRTD</b> | <b>MRTD Authentication Key</b> |
|-------------------------|--------------------------------|

The issuing State or Organization has to establish the necessary public key infrastructure in order to

- (i) generate the MRTD's Chip Authentication Key Pair,
- (ii) sign and store the Chip Authentication Public Key in the Chip Authentication Public Key data in EF.DG14 and
- (iii) support inspection systems of receiving States or organizations to verify the authenticity of the MRTD's chip used for genuine MRTD by certification of the Chip Authentication Public Key by means of the Document Security Object.

|                                   |   |
|-----------------------------------|---|
| <b>OE.Authoriz_Sens_Data Data</b> | <b>Authorization for Use of Sensitive Biometric Reference</b> |
|-----------------------------------|---|

The issuing State or Organization has to establish the necessary public key infrastructure in order to limit the access to sensitive biometric reference data of MRTD's holders to authorized receiving States or Organizations. The Country Verifying Certification Authority of the issuing State or Organization generates card verifiable Document Verifier Certificates for the authorized Document Verifier only.

|                       |                                  |
|-----------------------|----------------------------------|
| <b>OE.AA_Key_MRTD</b> | <b>Active Authentication Key</b> |
|-----------------------|----------------------------------|

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The issuing State or Organization has to establish the necessary public key infrastructure in order to

- (i) generate the MRTD's Active Authentication Key Pair,
- (ii) sign and store the Active Authentication Public Key in the Chip Authentication Public Key data in EF.DG15

|                              |  |
|------------------------------|--|
| <b>OE.AA_Personalization</b> | <b>Active Authentication Personalization</b> |
|------------------------------|--|

The Personalization Agents enable or disable the Active Authentication function of the TOE according to the decision of the issuing State or Organization. If the Active Authentication function is enabled the Personalization Agents generate the Active authentication keys and store them in the MRTD's chip.

|  |
|--|
| <b>Receiving State or organization</b> |
|--|

The Receiving State or Organization will implement the following security objectives of the TOE environment.

|                     |  |
|---------------------|--|
| <b>OE.Exam_MRTD</b> | <b>Examination of the MRTD passport book</b> |
|---------------------|--|

The inspection system of the Receiving State must examine the MRTD presented by the traveller to verify its authenticity by means of the physical security measures and to detect any manipulation of the physical MRTD. The Basic Inspection System for global interoperability

- (i) includes the Country Signing Public Key and the Document Signer Public Key of each issuing State or Organization, and
- (ii) implements the terminal part of the Basic Access Control [R1]. Additionally General Inspection Systems and Extended Inspection Systems perform the Chip Authentication Protocol to verify the Authenticity of the presented MRTD's chip.

|                              |   |
|------------------------------|---|
| <b>OE.Passive_Auth_Verif</b> | <b>Verification by Passive Authentication</b> |
|------------------------------|---|

The border control officer of the Receiving State uses the inspection system to verify the traveller as MRTD holder. The inspection systems must have successfully verified the signature of Document Security Objects and the integrity data elements of the logical MRTD before they are used. The receiving States and organizations must manage the Country Signing Public Key and the Document Signing Public Key maintaining their authenticity and availability in all inspection systems.

|                             |   |
|-----------------------------|---|
| <b>OE.Prot_Logical_MRTD</b> | <b>Protection of data of the logical MRTD</b> |
|-----------------------------|---|

The inspection system of the receiving State or Organisation ensures the confidentiality and integrity of the data read from the logical MRTD. The inspection system will prevent eavesdropping to their communication with the TOE before secure messaging is successfully established based on the Chip Authentication Protocol.

**Application note 16:**

The figure 2.1 in [R4] supposes that the **GIS** and the **EIS** follow the order

- (i) running the Basic Access Control Protocol,
- (ii) reading and verifying only those parts of the logical MRTD after which are necessary to know for the Chip Authentication Mechanism (i.e. Document Security Object and Chip Authentication Public Key),
- (iii) running the Chip Authentication protocol, and



- (iv) reading and verifying the less-sensitive data of the logical MRTD after Chip Authentication.

The supposed sequence has the advantage that the less sensitive data are protected by secure messaging with cryptographic keys based on the Chip Authentication Protocol which quality is under control of the TOE. The inspection system will prevent additionally eavesdropping to their communication with the TOE before secure messaging is successfully established based on the Chip Authentication Protocol. Note that reading the less-sensitive data directly after Basic Access Control Mechanism is allowed and is not assumed as threat in this PP. But the TOE ensures that reading of sensitive data is possible after successful Chip Authentication and Terminal Authentication Protocol only.

|                            |   |
|----------------------------|---|
| <b>OE.Ext_Insp_Systems</b> | <b>Authorisation of Extended Inspection Systems</b> |
|----------------------------|---|

The Document Verifier of receiving States or Organizations authorize Extended Inspection Systems by creation of Inspection System Certificates for access to sensitive biometric reference data of the logical MRTD. The Extended Inspection System authenticates themselves to the MRTD's chip for access to the sensitive biometric reference data with its private Terminal Authentication Key and its Inspection System Certificate.

## 6 Security Requirements

### 6.1 Extended Components Definition

This security target uses components defined as extensions to CC part 2. These components are defined in this security target.

They are the following:

- Family **FAU\_SAS**
- Family **FCS\_RND**
- Family **FIA\_API**
- Family **FMT\_LIM**
- Family **FPT\_EMSEC**

Definition of these families and related requirements is provided in the Protection Profile [R6].

### 6.2 Security Functional Requirements for the TOE

This section on security functional requirements for the TOE is divided into sub-section following the main security functionality.

#### 6.2.1 Class FAU Security Audit

The TOE shall meet the requirement “Audit storage (FAU\_SAS.1)” as specified below (Common Criteria Part 2).

#### **FAU\_SAS.1 Audit storage**

FAU\_SAS.1.1 The TSF shall provide the **[Manufacturer]** with the capability to store **[the IC Identification Data]** in the audit records.

Dependencies: No dependencies.

#### **Application note 20:**

The Manufacturer role is the default user identity assumed by the TOE in the Phase 2 Manufacturing. The IC manufacturer and the MRTD manufacturer in the Manufacturer role write the Initialisation Data and/or Pre-personalization Data as TSF Data of the TOE. The audit records are write-only-once data of the MRTD’s chip (see **FMT\_MTD.1/INI\_DIS**). The security measures in the manufacturing environment assessed under **ADO\_IGS** and **ADO\_DEL** ensure that the audit records will be used to fulfil the security objective **OD.Assurance**.

#### 6.2.2 Class Cryptographic Support (FCS)

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### 6.2.2.1 Cryptographic key generation (FCS\_CKM.1)

The TOE shall meet the requirement “Cryptographic key generation (FCS\_CKM.1)” as specified below (Common Criteria Part 2). The iterations are caused by different cryptographic key generation algorithms to be implemented and key to be generated by the TOE.

**FCS\_CKM.1/BAC\_MRTD Cryptographic key generation – Generation of Document Basic Access Keys by the TOE**

FCS\_CKM.1.1/ BAC\_MRTD The TSF shall generate cryptographic keys in accordance with a specified cryptographic key generation algorithm [**Document Basic Access Key Derivation Algorithm**] and specified cryptographic key sizes [**112 bits**] that meet the following: [**R1, normative appendix 5**].

Dependencies: [FCS\_CKM.2 Cryptographic key distribution or  
FCS\_COP.1 Cryptographic operation]  
FCS\_CKM.4 Cryptographic key destruction

**Application note 21:**

The TOE is equipped with the Document Basic Access Key generated and downloaded by the Personalization Agent. The Basic Access Control Authentication Protocol described in [R1], normative appendix 5, A5.2, produces agreed parameters to generate the Triple-DES key and the Retail-MAC BAC Session Keys for secure messaging by the algorithm in [R1], normative appendix 5, A5.1. The TOE uses this key derivation function to derive other session keys from shared secrets established by the Chip Authentication Protocol for the secure messaging required by **FCS\_COP.1/ENC\_MRTD** and **FCS\_COP.1/MAC\_MRTD** as well. The TOE may use this key derivation function for authentication of the Personalization Agent. The algorithm uses the random number RND.ICC generated by TSF as required by **FCS\_RND.1/MRTD**.

The TOE shall meet the requirement “Cryptographic key destruction (FCS\_CKM.4)” as specified below (Common Criteria Part 2).

**FCS\_CKM.1/DH MRTD Cryptographic key generation – Diffie-Hellman Keys by the MRTD**

FCS\_CKM.1.1/ The TSF shall generate cryptographic keys in accordance with a specified cryptographic key generation algorithm [**Diffie Hellman or Elliptic Curve Diffie Hellmann**] and specified cryptographic key sizes [**112 bits**] that meet the following: [**R4, Annex A.1**].

Dependencies: [FCS\_CKM.2 Cryptographic key distribution or  
FCS\_COP.1 Cryptographic operation]  
FCS\_CKM.4 Cryptographic key destruction

**Application note 22:**

The TOE generates a shared secret value with the terminal during the Chip Authentication Protocol, see [R4], sec. 3.1 and Annex A.1. This protocol may be based on the Diffie-Hellman-Protocol compliant to PKCS#3 (i.e. a modulo arithmetic based cryptographic algorithm, cf. [R18]) or on the ECDH

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compliant to ISO 15946 (i.e. an elliptic curve cryptography algorithm) (cf. [R4], Annex A.1, [R14] and [R17] for details). The shared secret value is used to derive the 112-bit Triple-DES key for encryption and the 112 bit Retail-MAC Chip Session Keys according to the Document Basic Access Key Derivation Algorithm [R1], annex E.1, for the TSF required by **FCS\_COP.1/ENC\_MRTD** and **FCS\_COP.1/MAC\_MRTD**.

**FCS\_CKM.1/ASYM Cryptographic key generation – Assymmetric keys for EAC**

FCS\_CKM.1.1/ The TSF shall generate cryptographic keys in accordance with a specified cryptographic key generation algorithm [*for RSA and ECC*] and specified cryptographic key sizes [ - 1024, 1536 and 2048 for RSA, - 192bits, 224bits, 256 bits, 384 bits and 512 bits over characteristic p curves for ECC ] that meet the following: [R23], [R24], [R25], [R26].

Dependencies: [FCS\_CKM.2 Cryptographic key distribution or FCS\_COP.1 Cryptographic operation]  
FCS\_CKM.4 Cryptographic key destruction  
FMT\_MSA.2 Secure security attributes

The TOE shall meet the requirement “Cryptographic key destruction (FCS\_CKM.4)” as specified below (Common Criteria Part 2).

**FCS\_CKM.4 Cryptographic key destruction - MRTD**

FCS\_CKM.4.1/ MRTD The TSF shall destroy cryptographic keys in accordance with a specified cryptographic key destruction method [*zeroisation*] that meets the following: [*no standard*].

Dependencies: [FDP\_ITC.1 Import of user data without security attributes, or FDP\_ITC.2 Import of user data with security attributes, or FCS\_CKM.1 Cryptographic key generation]

**Application note 23:**

The TOE shall destroy the BAC Session Keys

- (i) after detection of an error in a received command by verification of the MAC, and
- (ii) after successful run of the Chip Authentication Protocol.

The TOE shall destroy the Chip Session Keys after detection of an error in a received command by verification of the MAC. The TOE shall clear the memory area of any session keys before starting the communication with the terminal in a new power-on-session.

6.2.2.2 Cryptographic operation (FCS\_COP.1)

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The TOE shall meet the requirement “Cryptographic operation (FCS\_COP.1)” as specified below (Common Criteria Part 2). The iterations are caused by different cryptographic algorithms to be implemented by the TOE.

**FCS COP.1/SIG MRTD Cryptographic operation – signature by MRTD**

The TSF shall perform **[digital signature creation]** in accordance with a specified cryptographic algorithm **[RSA CRT or ECDSA with SHA1, SHA-224, SHA-256 or SHA-384]** and cryptographic key sizes **[**

- 1024, 1536 and 2048 bits for RSA CRT,
  - 192, 256 384 and 512 bits for ECDSA,
- ]** that meet the following: **[**
- scheme 1 of ISO/IEC 9796-2:2002 for RSA CRT,
  - **[R15], [R16], [R14]** for ECDSA,
- ]**.

Dependencies: [FDP\_ITC.1 Import of user data without security attributes, or  
 FDP\_ITC.2 Import of user data with security attributes, or  
 FCS\_CKM.1 Cryptographic key generation]  
 FCS\_CKM.4 Cryptographic key destruction  
 FMT\_MSA.2 Secure security attributes

**FCS COP.1/SHA MRTD Cryptographic operation – Hash for Key Derivation by MRTD**

FCS\_COP.1.1/SHA\_MRTD The TSF shall perform **[hashing]** in accordance with a specified cryptographic algorithm **[SHA-1, SHA-224, SHA-256 and SHA-384]** and cryptographic key sizes **[none]** that meet the following: **[FIPS 180-2]**.

Dependencies: [FDP\_ITC.1 Import of user data without security attributes, or  
 FDP\_ITC.2 Import of user data with security attributes, or  
 FCS\_CKM.1 Cryptographic key generation]  
 FCS\_CKM.4 Cryptographic key destruction  
 FMT\_MSA.2 Secure security attributes

**Application note 24:**

This SFR requires the TOE to implement the hash function SHA-1 for the cryptographic primitive of the Session key derivation used by the Basic Access Control Authentication Mechanism and the chip authentication mechanism.

**FCS COP.1/TDES MRTD Cryptographic operation – Encryption / Decryption Triple DES**

FCS\_COP.1.1/TDES\_MRTD The TSF shall perform **[secure messaging – encryption and decryption]** in accordance with a specified cryptographic algorithm **[Triple-DES in CBC mode]** and cryptographic key sizes **[112 bits]** that meet the following: **[FIPS 46-3 [R19] and [R5]; Annex E]**.

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Dependencies:

[FDP\_ITC.1 Import of user data without security attributes, or  
FDP\_ITC.2 Import of user data with security

attributes, or

FCS\_CKM.1 Cryptographic key generation]  
FCS\_CKM.4 Cryptographic key destruction  
FMT\_MSA.2 Secure security attributes

**Application note 25:**

This SFR requires the TOE to implement the cryptographic primitive for secure messaging with encryption of the transmitted data. The keys are agreed between the TOE and the terminal as part of

- (i) the Basic Access Control Authentication Mechanism according to the **FCS\_CKM.1/BAC\_MRTD** or
- (ii) the Chip Authentication Protocol according to the **FCS\_CKM.1/DH\_MRTD**.

Note the Triple-DES in CBC mode with zero initial vector include also the Triple-DES in ECB mode for blocks of 8 byte used to check the authentication attempt of a terminal as Personalization Agent by means of the symmetric authentication mechanism.

**FCS COP.1/MAC MRTD Cryptographic operation – Retail MAC**

FCS\_COP.1.1/MAC\_MRTD The TSF shall perform [**secure messaging – message authentication code**] in accordance with a specified cryptographic algorithm [**Retail MAC**] and cryptographic key sizes [**112 bits**] that meet the following: [**ISO 9797 (MAC algorithm 3, block cipher DES, Sequence Message Counter, padding mode 2)**].

Dependencies:

[FDP\_ITC.1 Import of user data without security attributes , or  
FDP\_ITC.2 Import of user data with security

attributes, or

FCS\_CKM.1 Cryptographic key generation]  
FCS\_CKM.4 Cryptographic key destruction  
FMT\_MSA.2 Secure security attributes

**Application note 26:**

This SFR requires the TOE to implement the cryptographic primitive for secure messaging with encryption and message authentication code over the transmitted data. The key is agreed between the TSF by the Basic Access Control Authentication Mechanism as part of

- (i) the Basic Access Control Authentication Mechanism according to the **FCS\_CKM.1/BAC\_MRTD** or
- (ii) the Chip Authentication Protocol according to the **FCS\_CKM.1/DH\_MRTD**.

**FCS COP.1/SIG\_VER Cryptographic operation – Signature verification by MRTD**

FCS\_COP.1.1/SIG\_VER

The TSF shall perform [**digital signature verification**] in accordance with a specified cryptographic algorithm [**RSASSA-PKCS1-v1\_5 or RSASSA-PSS or ECDSA with SHA-1, SHA-224, SHA-256 or SHA-384**] and cryptographic key sizes [**- 1024, 1536 and 2048 for RSA,**

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- 192bits, 224bits, 256 bits, 384 bits and 512 bits over characteristic  $p$  curves for ECC] that meet the following: [  
- [R21] and [R22] for RSA,  
- [R15], [R16], [R14] for ECC]

Dependencies: [FDP\_ITC.1 Import of user data without security attributes, or FDP\_ITC.2 Import of user data with security attributes, or  
FCS\_CKM.1 Cryptographic key generation]  
FCS\_CKM.4 Cryptographic key destruction  
FMT\_MSA.2 Secure security attributes

### **Random Number Generation (FCS\_RND.1)**

The TOE shall meet the requirement “Quality metric for random numbers (FCS\_RND.1)” as specified below (Common Criteria Part 2 extended).

#### **FCS\_RND.1/MRTD Quality metric for random numbers**

FCS\_RND.1.1/MRTD, the TSF shall provide a mechanism to generate random numbers that meet **[the requirement to provide an entropy of at least 7.976 bit in each byte, following AIS 31 [R27] ]**.

Dependencies: No dependencies.

#### **Application note 28:**

This SFR requires the TOE to generate random numbers used for the authentication protocols as required by **FIA\_UAU.4/MRTD**.

#### *6.2.3 Class FIA Identification and Authentication*

#### **Application note 29:**

The Table 1 provides an overview on the authentication mechanisms used.

|                       |                    |                          |              |
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| Name  | SFR for the TOE                                | SFR for the TOE environment (terminal)      | Algorithms and key sizes according to [R1], Annex E, and [R4]                                  |
|---|--|---|--|
| Basic Access Control Authentication Mechanism                 | FIA_UAU.4/MRTD and FIA_UAU.6/MRTD FIA_AFL.1    | FIA_UAU.4/BT and FIA_UAU.6/T                | Triple-DES, 112 bits keys and Retail-MAC, 112 bit keys   |
| Symmetric Authentication Mechanism for Personalization Agents | FIA_UAU.4/MRTD                                 | FIA_API.1/PT                                | Triple-DES with 112 bits keys  |
| Active Authentication Mechanism (if enabled)                  | FIA_API.1/AA                                   | FIA_UAU.4/BT                                | RSA with 1024, 1536, 2048 bits or ECC with 192, 256, 384, 512 bits according to [R1], Annex D. |
| Chip authentication protocol                                  | FIA_API.1/MRTD, FIA_UAU.5/MRTD, FIA_UAU.6/MRTD | FIA_UAU.4/GIS, FIA_UAU.5/GIS, FIA_UAU.6/GIS | DH or ECDH and Retail-MAC, 112 bit keys  |
| Terminal authentication protocol                              | FIA_UAU.5/MRTD                                 | FIA_API.1/EIS                               | RSASSA-PKCS1-v1_5 and RSASSA-PSS ECDSA with SHA (SHA-1, SHA-224, SHA-256 or SHA-384)           |

**Table 1: Overview on authentication SFR**

### **FIA\_UID.1 Timing of identification**

FIA\_UID.1.1 The TSF shall allow

**(1) to establish the communication channel**

**(2) to read the initialization data if it is not disabled by TSF according to FMT\_MTD.1/INI\_DIS** on behalf of the user to be performed before the user is identified.

FIA\_UID.1.2 The TSF shall require each user to be successfully identified before allowing any other TSF-mediated actions on behalf of that user.

Dependencies: No dependencies.

**Application note 30:**

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The MRTD's chip and the terminal establish the communication channel through the contactless. The Protocol Type A defines an "Answer to Select" (ATS) and the protocol Type B is managed through the commands "Answer to Request" and "Answer to Attrib". Note that the terminal and the MRTD's chip use an identifier for the communication channel to allow the terminal for communication with more than one RFID. If the historical bytes are used to identify the product as usual for example with hard-mask version and component code (specific to the manufacturer), in particular context this could lead to an exploitation of the threat **T.Chip\_Id** (e.g. in the case a MRTD holder has a chip manufactured by a local manufacturer, he could be traced in a foreign country where few holders could have the same ATS content). Therefore the ATS has to be set in such a manner, that it will not lead to a vulnerability by the means of identifying the chip (e.g. randomly using random number generator as required by **FCS\_RND.1**).

#### **Application note 31:**

In the "Operation Use" phase the MRTD must not allow anybody to read the ICCSN or any other unique identification before the user is authenticated as Basic Inspection System (cf. **T.Chip\_ID**). Note, that the terminal and the MRTD's chip use an identifier for the communication channel to allow the terminal for communication with more than one RFID. If this identifier is randomly selected it will not violate the **OT.Identification**. If this identifier is fixed the ST writer should consider the possibility to misuse this identifier to perform attacks addressed by **T.Chip\_ID**.

In the TOE, the chip identifier cannot be read in the operational phase, and the UID is randomized at each session

#### **Application note 32:**

In the Phase 2 "Manufacturing of the TOE" the Manufacturer is the only user role known to the TOE which writes the Initialization Data and/or Pre-personalization Data in the audit records of the IC. The MRTD manufacturer may create the user role Personalization Agent for transition from Phase 2 to Phase 3 "Personalization of the MRTD". The users in role Personalization Agent identify themselves by means of selecting the authentication key. After personalization in the Phase 3 the user role **Basic Inspection System** is created by writing the Document Basic Access Keys. The **Basic Inspection System** is identified as default user after power up or reset of the TOE i.e. the TOE will use the Document Basic Access Key to authenticate the user as **Basic Inspection System**. After successful authentication as **Basic Inspection System** the terminal may identify themselves as

- (i) Extended Inspection System by selection of the templates for the Terminal Authentication Protocol or
- (ii) if necessary and available as Personalization Agent by selection of the Personalization Agent Authentication Key.

#### **FIA\_UAU.1 Timing of authentication**

FIA\_UAU.1.1 The TSF shall allow

- (1) to establish the communication channel**
- (2) to read the initialization data if it is not disabled by TSF according to FMT\_MTD.1/INI\_DIS**
- (3) to identify themselves by selection of the authentication key**

On behalf of the user to be performed before the user is authenticated.

FIA\_UAU.1.2 The TSF shall require each user to be successfully authenticated before allowing any other TSF-mediated actions on behalf of that user.

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Dependencies:

FIA\_UID.1 Timing of identification.

**FIA\_API.1/AA Authentication Proof of Identity - MRTD**

FIA\_API.1.1/AA The TSF shall provide an **[Active Authentication Protocol]** to prove the identity of the TOE.

Dependencies: No dependencies.

**FIA\_UAU.4/MRTD Single-use authentication mechanisms - Single-use authentication of the Terminal by the TOE**

FIA\_UAU.4.1/MRTD The TSF shall prevent reuse of authentication data related to

- 1. Basic Access Control Authentication Mechanism,**
- 2. Terminal Authentication protocol,**
- 3. Authentication Mechanism based on Triple-DES.**

Dependencies: No dependencies.

**Application note 33:**

All listed authentication mechanisms uses a challenge of 8 Bytes freshly and randomly generated by the TOE to prevent reuse of a response generated by a terminal in a successful authentication attempt: The Basic Access Control Authentication Mechanism, the Terminal Authentication Protocol and the Authentication Mechanism based on Triple-DES use RND.ICC [R4].

**Application note 34:**

The Basic Access Control Mechanism is a mutual device authentication mechanism defined in [R1]. In the first step the terminal authenticates itself to the MRTD's chip and the MRTD's chip authenticates to the terminal in the second step. In the first step the TOE sends a randomly chosen challenge which shall contain sufficient entropy to prevent **T.Chip\_ID**. In the second step the MRTD's chip provides a challenge-response-pair which allows the terminal a unique identification of the MRTD's chip with some probability depending on the entropy of the Document Basic Access Keys. Therefore the TOE shall stop the communication with the terminal not successfully authenticated in the first step of the protocol to fulfil the security objective **OT.Identification** and to prevent **T.Chip\_ID**.

**FIA\_UAU.5 Multiple authentication mechanisms**

FIA\_UAU.5.1 The TSF shall provide:

- 1. Basic Access Control Authentication Mechanism**
- 2. Terminal Authentication protocol**
- 3. Secure messaging in MAC-ENC mode,**
- 4.. Symmetric Authentication Mechanism based on Triple-DES**

to support user authentication.

FIA\_UAU.5.2 The TSF shall authenticate any user's claimed identity according to the following rules:

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1. **The TOE accepts the authentication attempt as Personalization Agent by one of the following mechanisms**
  - (a) **the Basic Access Control Authentication Mechanism with the Personalization Agent Keys,**
  - (b) **the Symmetric Authentication Mechanism with the Personalization Agent Key**
  - (c) **the Terminal Authentication Protocol with Personalization Agent Keys.**
2. **The TOE accepts the authentication attempt as Basic Inspection System only by means of the Basic Access Control Authentication Mechanism with the Document Basic Access Keys.**
3. **After successful authentication as Basic Inspection System and until the completion of the Chip authentication Mechanism the TOE accepts only received command with correct message authentication code sent by means of secure messaging with the key agreed upon with the authenticated terminal by means of the Basic Access Control Authentication Mechanism.**
4. **After run of the Chip Authentication Mechanism the TOE accepts only received commands with correct message authentication code sent by means of secure messaging with key agreed with the terminal by means of the Chip Authentication Mechanism.**
5. **The TOE accepts the authentication attempt by means of the Terminal Authentication Protocol only if the terminal uses secure messaging established by the Chip Authentication Mechanism.**

**Refinement:**

**The TOE authenticates the Personalization agent by a Symmetric Authentication Mechanism with Personalizer Agent Key**

Dependencies: No dependencies.

**Application note 35:**

Depending on the authentication methods used the **Personalization Agent** holds

- (i) a pair of a Triple-DES encryption key and a retail-MAC key for the Basic Access Control Mechanism specified in [R1], or
- (ii) a Triple-DES key for the Symmetric Authentication Mechanism or
- (iii) an asymmetric key pair for the Terminal Authentication Protocol (e.g. provided by the Extended Access Control PKI in a valid card verifiable certificate with appropriate encoded access rights).

The Basic Access Control Mechanism includes the secure messaging for all commands exchanged after successful authentication of the inspection system. The Personalization Agent may use Symmetric Authentication Mechanism without secure messaging mechanism as well if the personalization environment prevents eavesdropping to the communication between TOE and personalization terminal. The **Basic Inspection System** shall use the Basic Access Control Authentication Mechanism with the Document Basic Access Keys and the secure messaging after the mutual authentication. The **General Inspection System** shall use the secure messaging with the keys generated by the Chip Authentication Mechanism.

For the TOE, the option (a) of the SFR is not available: Personalisation agent can only be authenticated using the Symmetric Authentication Mechanism with the Personalization Agent Key.

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#### **FIA\_UAU.6/MRTD Re-authenticating – Re-authenticating of Terminal by the TOE**

FIA\_UAU.6.1/MRTD The TSF shall re-authenticate the user under the conditions:

**1. Each command sent to TOE after successful authentication of the terminal with Basic Access Control Authentication Mechanism and until the completion of the Chip Authentication Mechanism shall be verified as being sent by the authenticated BIS.**

**2. Each command sent to the TOE after successful run of the Chip Authentication Protocol shall be verified as being sent by the GIS.**

Dependencies: No dependencies.

#### **Application note 36:**

The Basic Access Control Mechanism and the Chip Authentication Protocol specified in [R1] include secure messaging for all commands exchanged after successful authentication of the Inspection System. The TOE checks by secure messaging in MAC\_ENC mode each command based on Retail-MAC whether it was sent by the successfully authenticated terminal (see **FCS\_COP.1/MAC\_MRTD** for further details). The TOE does not execute any command with incorrect message authentication code. Therefore the TOE re-authenticates the user for each received command and accept only those commands received from the initially authenticated user.

#### **FIA\_AFL.1/Authentication failure handling – BAC Authentication**

FIA\_AFL.1.1/BAC The TSF shall detect when **[an administrator configurable positive integer within range of acceptable value 1 to 255 consecutive]** unsuccessful authentication attempts occur related to **[BAC Authentication protocol]**.

Application note : This positive integer is set in personalisation phase by **the Personalization Agent**

FIA\_AFL.1.2/BAC When the defined number of unsuccessful authentication attempts has been met or surpassed, the TSF shall **[increase the period of time needed to perform the BAC Authentication protocol]**.

#### **FIA\_AFL.1/Authentication failure handling – Terminal Authentication**

FIA\_AFL.1.1/TA The TSF shall detect when **[an administrator configurable positive integer within range of acceptable value 1 to 255 consecutive]** unsuccessful authentication attempts occur related to **[Terminal authentication]**.

Application note : This positive integer is set in personalisation phase by **the Personalization Agent**

FIA\_AFL.1.2/ TA When the defined number of unsuccessful authentication attempts has been met or surpassed, the TSF shall **[increase the period of time needed to perform the Terminal authentication]**.

Dependencies:

FIA\_UAU.1 Timing of authentication

**FIA\_API.1/CAP Authentication Proof of Identity - MRTD**

FIA\_API.1.1/CAP The TSF shall provide a **[Chip Authentication Protocol according to [R4] ]** to prove the identity of the **[TOE]**.

Dependencies: No dependencies.

**Application note 38:**

This SFR requires the TOE to implement the Chip Authentication Mechanism specified in [R4]. The TOE and the terminal generate a shared secret using the Diffie-Hellman Protocol (DH or EC-DH) and two session keys for secure messaging in ENC\_MAC mode according to [R1], Annex E.1. The terminal verifies by means of secure messaging whether the MRTD's chip was able or not to run his protocol properly using its Chip Authentication Private Key corresponding to the Chip Authentication Key (EF.DG14).

6.2.4 Class FDP User Data Protection

**Subset access control (FDP\_ACC.1)**

The TOE shall meet the requirement "Subset access control (FDP\_ACC.1)" as specified below (Common Criteria Part 2).

**FDP\_ACC.1 Subset access control**

FDP\_ACC.1.1 The TSF shall enforce the **[Access Control SFP]** on **[terminals gaining write, read and modification access to EF.COM, EF.SOD, EF.DG1 to EF.DG16 and Active Authentication Private Key of the logical MRTD]**.

Dependencies:

FDP\_ACF.1 Security attribute based access control

**Application note 39:**

The Basic Access Control SFP addresses the configuration of the TOE for usage with Basic Inspection Systems only.

**Security attribute based access control (FDP\_ACF.1)**

The TOE shall meet the requirement "Security attribute based access control (FDP\_ACF.1)" as specified below (Common Criteria Part 2).

**FDP\_ACF.1 Security attribute based access control**

FDP\_ACF.1.1 The TSF shall enforce the **[Access Control SFP]** to objects based on the following:

**1. Subjects:**

- a. Personalization Agent,**
- b. Basic Inspection System,**
- c. Extended Inspection System**
- c. Terminal,**

**2. Objects:**

- a. data in EF.DG1 to EF.DG16 of the logical MRTD**
- b. data in EF.COM**
- c. data in EF.SOD**
- d. Active Authentication Private Key**

**3. Security attributes**

- a. Authentication status of terminals,**
- b. Terminal Authorization.**

FDP\_ACF.1.2 The TSF shall enforce the following rules to determine if an operation among controlled subjects and controlled objects is allowed:

**1. The successfully authenticated Personalization Agent is allowed to write and to read the data of the data of the EF.COM, EF.SOD, EF.DG1 to EF.DG16 of the logical MRTD, including the Active Authenticate Public Key**

**2. the successfully authenticated Basic Inspection System is allowed to read the data in EF.COM, EF.SOD, EF.DG1, EF.DG2 and EF.DG5 to EF.DG16 of the logical MRTD**

**3. the successfully authenticated Extended Inspection System is allowed to read the data in EF.COM, EF.SOD, EF.DG1, EF.DG2 and EF.DG5 to EF.DG16 of the logical MRTD,**

**4. the successfully authenticated Extended Inspection System is allowed to read data in the EF.DG3 according to the Terminal Authorization,**

**5. the successfully authenticated Extended Inspection System is allowed to read data in the EF.DG4 according to the Terminal Authorization**

FDP\_ACF.1.3 The TSF shall explicitly authorize access of subjects to objects based on the following additional rules: **[none]**.

FDP\_ACF.1.4 The TSF shall explicitly deny access of subjects to objects based on the rule:

**1. A terminal authenticated as CVCA is not allowed to read data in the EF.DG3,**

**2. A terminal authenticated as CVCA is not allowed to read data in the EF.DG4,**

**3. A terminal authenticated as DV is not allowed to read data in the EF.DG3,**

**4. A terminal authenticated as DV is not allowed to read data in the EF.DG4,**

**5. the Terminals are not allowed to modify any of the EF.DG1 to EF.DG16 of the logical MRTD.**

Dependencies:

FDP\_ACC.1 Subset access control

FMT\_MSA.3 Static attribute initialisation

**Application note 40:**

The TOE verifies the certificate chain established by the Country Verifier Certification Authority, the Document Verifier Certificate and the Inspection System Certificate (cf. **FMT\_MTD.3**). The Terminal Authorization is the intersection of the Certificate Holder Authorization in the certificates of the Country

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Verifier Certification Authority, the Document Verifier Certificate and the Inspection System Certificate in a valid certificate chain.

### Inter-TSF-Transfer

The TOE shall meet the requirement “Basic data exchange confidentiality (FDP\_UCT.1)” as specified below (Common Criteria Part 2).

#### FDP\_UCT.1/MRTD Basic data exchange confidentiality - MRTD

FDP\_UCT.1.1/MRTD The TSF shall enforce the **[Access Control SFP]** to be able to **[transmit and receive]** user data in a manner protected from unauthorised disclosure **[after Chip Authentication]**.

Dependencies: FTP\_ITC.1 Inter-TSF trusted channel, or FTP\_TRP.1 Trusted path]  
[FDP\_ACC.1 Subset access control, or FDP\_IFC.1 Subset information flow control]

#### FDP\_UIT.1/MRTD Data exchange integrity - MRTD

FDP\_UIT.1.1/MRTD The TSF shall enforce the **[Access Control SFP]** to be able to **[transmit and receive]** user data in a manner protected from **[modification, deletion, insertion and replay]** errors after Chip Authentication.

FDP\_UIT.1.2/MRTD The TSF shall be able to determine on receipt of user data, whether **[modification, deletion, insertion and replay]** has occurred after Chip Authentication.

Dependencies: [FDP\_ACC.1 Subset access control, or FDP\_IFC.1 Subset information flow control]  
[FTP\_ITC.1 Inter-TSF trusted channel, or FTP\_TRP.1 Trusted path]

#### FDP\_ITC.1/AA Import of user data without security attributes

This requirement deals with the import of Active Authentication private RSA or ECC key, when it is not generated on card. It is applicable for TOE with or without BAC.

FDP\_ITC.1.1/AA The TSF shall enforce the **[Access Control SFP]** when importing user data, controlled under the SFP, from outside of the TSC.

FDP\_ITC.1.2/AA The TSF shall ignore any security attributes associated with the user data when imported from outside the TSC.

FDP\_ITC.1.3 The TSF shall enforce the following rules when importing user data controlled under the SFP from outside the TSC: **[none]**.

Dependencies: [FDP\_ACC.1 Subset access control, or  
FDP\_IFC.1 Subset information flow control]  
FMT\_MSA.3 Static attribute initialization

#### 6.2.5 Class FMT Security Management

#### **FMT\_MOF.1/AA Management of functions in TSF**

FMT\_MOF.1.1 The TSF shall restrict the ability to **[enable and disable]** the functions **[TSF Active Authentication]** to **[Personalization Agent]**.

#### **Refinement:**

Once the TOE is delivered to the Personalization agent, the TSF Active Authentication is not enabled. It can either let it disabled, or enable it by writing a lock. Once enabled, the TSF Active Authentication can not be disabled.

Dependencies: FMT\_SMF.1 Specification of management Functions  
FMT\_SMR.1 Security roles

#### **FMT\_SMF.1 Specification of Management Functions**

FMT\_SMF.1.1 The TSF shall be capable of performing the following security management functions:  
**1. Initialization,**  
**2. Personalization,**  
**3. Configuration**

Dependencies: No dependency

#### **FMT\_SMR.1 Security roles**

FMT\_SMR.1.1 The TSF shall maintain the roles  
**1. Manufacturer,**  
**2. Personalization Agent,**  
**5. Country Verifier Certification Authority,**  
**6. Document Verifier,**  
**7. Basic Inspection System,**  
**8. Domestic Extended Inspection System**  
**9. Foreign Extended Inspection System**

FMT\_SMR.1.2 The TSF shall be able to associate users with roles.

Dependencies: FIA\_UID.1 Timing of identification

**Application note 43:**

The **SFR FMT\_LIM.1** and **FMT\_LIM.2** address the management of the TSF and TSF data to prevent misuse of test features of the TOE over the life cycle phases.

**FMT\_LIM.1 Limited capabilities**

FMT\_LIM.1.1 The TSF shall be designed in a manner that limits their capabilities so that in conjunction with “Limited availability (FMT\_LIM.2)” the following policy is enforced:

***Deploying Test Features after TOE Delivery does not allow***

- 1. User Data to be disclosed or manipulated***
- 2. TSF data to be disclosed or manipulated***
- 3. software to be reconstructed and***
- 4. substantial information about construction of TSF to be gathered which may enable other attacks***

Dependencies: FMT\_LIM.2 Limited availability.

**FMT\_LIM.2 Limited availability**

FMT\_LIM.2.1 The TSF shall be designed in a manner that limits their availability so that in conjunction with “Limited capabilities (FMT\_LIM.1)” the following policy is enforced:

***Deploying Test Features after TOE Delivery does not allow***

- 1. User Data to be disclosed or manipulated,***
- 2. TSF data to be disclosed or manipulated***
- 3. Software to be reconstructed and***
- 4. Substantial information about construction of TSF to be gathered which may enable other attacks.***

Dependencies: FMT\_LIM.1 Limited capabilities.

**Application note 44:**

The following SFR are iterations of the component Management of TSF data (FMT\_MTD.1). The TSF data include but are not limited to those identified below.

**FMT\_MTD.1/INI\_ENA Management of TSF data – Writing of Initialisation Data and Prepersonalisation Data**

FMT\_MTD.1.1/INI\_ENA The TSF shall restrict the ability to ***[write]*** the ***[Initialisation Data and Prepersonalisation Data]*** to ***[the Manufacturer]***.

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Dependencies: FMT\_SMF.1 Specification of management functions  
FMT\_SMR.1 Security roles

**Application note 45:**

The pre-personalization Data includes but is not limited to the authentication reference data for the Personalization Agent that is the symmetric cryptographic Personalization Agent Authentication Key.

**FMT\_MTD.1/INI\_DIS Management of TSF data – Disabling of Read Access to Initialisation Data and Pre-personalization Data**

FMT\_MTD.1.1/ INI\_DIS The TSF shall restrict the ability to disable **[read access for users]** to the **[Initialisation Data]** to **[the Personalization Agent]**.

Dependencies: FMT\_SMF.1 Specification of management functions  
FMT\_SMR.1 Security roles

**Application note 46:**

According to **P.Manufact** the IC Manufacturer and the MRTD Manufacturer are the default users assumed by the TOE in the role Manufacturer during the Phase 2 “Manufacturing” but the TOE is not requested to distinguish between these users within the role Manufacturer. The TOE may restrict the ability to write the Initialization Data and the Pre-personalization Data by

- (i) allowing to write these data only once and
- (ii) blocking the role Manufacturer at the end of the Phase 2.

The IC Manufacturer may write the Initialization Data which includes but are not limited to the IC Identifier as required by **FAU\_SAS.1**. The Initialization Data provides a unique identification of the IC which is used to trace the IC in the Phase 2 and 3 “personalization” but is not needed and may be misused in the Phase 4 “Operational Use”. Therefore the external read access shall be blocked. The MRTD Manufacturer will write the Pre-personalization Data.

**FMT\_MTD.1/CVCA\_INI Management of TSF data – Initialisation of CVCA Certificate and Current Date**

FMT\_MTD.1.1/ CVCA\_INI The TSF shall restrict the ability to **[write]** the

1. Initial Country Verifying Certification Authority Public Key,
2. Initial Country Verifier Certification Authority Certificate,
3. Initial Current Date

to **[The Personalization Agent]**.

Dependencies: FMT\_SMF.1 Specification of management functions FMT\_SMR.1 Security roles

**FMT\_MTD.1/CVCA\_UPD Management of TSF data – Country Verifier Certification Authority**

FMT\_MTD.1.1/ CVCA\_UPD The TSF shall restrict the ability to **[update]** the

1. Country Verifier Certification Authority Public Key,
2. Country Verifier Certification Authority Certificate

to **[Country Verifier Certification Authority]**.



Dependencies: FMT\_SMF.1 Specification of management functions  
 FMT\_SMR.1 Security roles

**FMT\_MTD.1/DATE Management of TSF data – Current date**

FMT\_MTD.1.1/ The TSF shall restrict the ability to **[modify]** the **[Current date]** to

1. Country Verifier Certification Authority,
2. Document Verifier,
3. domestic Extended Inspection System

Dependencies: FMT\_SMF.1 Specification of management functions  
 FMT\_SMR.1 Security roles

**FMT\_MTD.1/KEY\_WRITE Management of TSF data – Key Write**

FMT\_MTD.1.1/KEY\_WRITE The TSF shall restrict the ability to **[write]** the **[Document Basic Access Keys and the Active Authentication RSA or ECC private key]** to **[the Personalization Agent.]**

Dependencies: FMT\_SMF.1 Specification of management functions  
 FMT\_SMR.1 Security roles

**FMT\_MTD.1/CAPK Management of TSF data – Chip Authentication Private Key**

FMT\_MTD.1.1/CAPK creation The TSF shall restrict the ability to **[create]** the Chip Authentication Private Key to **[The manufacturer Agent].**

FMT\_MTD.1.1/CAPK loading The TSF shall restrict the ability to **[load]** the Chip Authentication Private Key to **[The Personalization Agent].**

**Refinement:**

The Manufacturer agent creates the chip authentication key needed by the TOE during the initialisation phase (phase 6). Once it was successfully done, the Personalization agent can not create Chip authentication private key(s), as they will not be used by the TOE. Therefore, by construction, only the manufacturer agent can create the chip authentication key.

Dependencies: FMT\_SMF.1 Specification of management functions  
 FMT\_SMR.1 Security roles

**FMT\_MTD.1/KEY\_READ Management of TSF data – Key Read**

FMT\_MTD.1.1/KEY\_READ The TSF shall restrict the ability to **[read]** the

1. **Document Basic Access Keys,**
2. **the Active Authentication RSA or ECC private key**
3. **the Chip authentication private key**
4. **Personalization Agent Keys**

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to **[none]**.

Dependencies: FMT\_SMF.1 Specification of management functions  
FMT\_SMR.1 Security roles

**FMT\_MTD.3 Secure TSF data**

FMT\_MTD.3.1 The TSF shall ensure that only secure values **[of the certificate chain]** are accepted for TSF data **[of the Terminal Authentication Protocol and the Access Control]**.

Dependencies: ADV\_SPM.1 Informal TOE security policy model  
FMT\_MTD.1 Management of TSF data

**Refinement:**

The certificate chain is valid if and only if

- (1) the digital signature of the Inspection System Certificate can be verified as correct with the public key of the Document Verifier Certificate and the expiration date of the Inspection System Certificate is not before the Current Date of the TOE,
- (2) the digital signature of the Document Verifier Certificate can be verified as correct with the public key in the Certificate of the Country Verifying Certification Authority and the expiration date of the Document Verifier Certificate is not before the Current Date of the TOE,
- (3) the digital signature of the Certificate of the Country Verifying Certification Authority can be verified as correct with the public key of the Country Verifying Certification Authority known to the TOE and the expiration date of the Certificate of the Country Verifying Certification Authority is not before the Current Date of the TOE.

The Inspection System Public Key contained in the Inspection System Certificate in a valid certificate chain is a secure value for the authentication reference data of the Extended Inspection System. The intersection of the Certificate Holder Authorizations contained in the certificates of a valid certificate chain is a secure value for Terminal Authorization of a successful authenticated Extended Inspection System.

**Application note 52:**

The Terminal Authentication is used for Extended Inspection System as required by **FIA\_UAU.4** and **FIA\_UAU.5**. The Terminal Authorization is used as TSF data for access control required by **FDP\_ACF.1**

## 6.2.6 Class FPT Protection of the Security functionalities

The TOE shall prevent inherent and forced illicit information leakage for User Data and TSF Data. The security functional requirement **FPT\_EMSEC.1** addresses the inherent leakage. With respect to the forced leakage they have to be considered in combination with the security functional requirements “Failure with preservation of secure state (**FPT\_FLS.1**)” and “TSF testing (**FPT\_TST.1**)” on the one hand and “Resistance to physical attack (**FPT\_PHP.3**)” on the other. The SFR “Non-bypass ability of the TSP (**FPT\_RVM.1**)” and “TSF domain separation (**FPT\_SEP.1**)” together with “Limited capabilities (**FMT\_LIM.1**)”, “Limited availability (**FMT\_LIM.2**)” and “Resistance to physical attack (**FPT\_PHP.3**)” prevent bypassing, deactivation and manipulation of the security features or misuse of TOE functions.

### FPT\_EMSEC.1 TOE Emanation

FPT\_EMSEC.1.1 The TOE shall not emit **[power variations, timing variations during command execution]** in excess of **[non useful information]** enabling access to **[personalization agent Authentication Key and Chip Authentication Private Key]** and **[Active Authentication RSA or ECC private key]**

FPT\_EMSEC.1.2 The TSF shall ensure **[any unauthorized users]** are unable to use the following interface **[smart card circuit contacts]** to gain access to **[Personalization Agent Authentication Key and Chip Authentication Private Key]** and **[Active Authentication RSA or ECC private key]**

Dependencies: No other components.

### Application note 53

The ST writer shall perform the operation in **FPT\_EMSEC.1.1** and **FPT\_EMSEC.1.2**. The TOE shall prevent attacks against the listed secret data where the attack is based on external observable physical phenomena of the TOE. Such attacks may be observable at the interfaces of the TOE or may origin from internal operation of the TOE or may origin by an attacker that varies the physical environment under which the TOE operates. The set of measurable physical phenomena is influenced by the technology employed to implement the smart card. The MRTD’s chip has to provide a smart card contact less interface but may have also (not used by the terminal but maybe by an attacker) additional contacts according to ISO/IEC 7816-2 as well. Examples of measurable phenomena include, but are not limited to variations in the power consumption, the timing of signals and the electromagnetic radiation due to internal operations or data transmissions.

The following security functional requirements address the protection against forced illicit information leakage including physical manipulation.

### FPT\_FLS.1 Failure with preservation of secure state

FPT\_FLS.1.1 The TSF shall preserve a secure state when the following types of failures occur:  
**(1) Exposure to operating conditions where therefore a malfunction could occur,**  
**(2) failure detected by TSF according to FPT\_TST.1.**

### Refinement:

In particular, (1) means the TOE handles the tearing, or loss of field.

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Dependencies:

No dependencies

**FPT\_TST.1 TSF testing**

FPT\_TST.1.1 The TSF shall run a suite of self tests **[selection : during initial start up, periodically during normal operation, at the request of the authorised user, at the conditions [assignment : conditions under which the self test should occur]]** to demonstrate the correct operation of the TSF.

FPT\_TST.1.2 The TSF shall provide authorised users with the capability to verify the integrity of TSF data.

FPT\_TST.1.3 The TSF shall provide authorised users with the capability to verify the integrity of stored TSF executable code.

**Note:**

The instantiation of this requirement is provided in the following table:

**Refinement for FPT\_TST.1.1:**

| <b>Selection and assignments : conditions under which self test should occur</b> |
|--|
| At reset   |
| Before the first execution of the optional code                                  |
| After the Active Authentication is computed                                      |
| Before any cryptographic operation   |
| When accessing a DG or the files EF.CVCA or EF.TOE_IDentification                |
| Prior to any use of TSF data   |
| Before execution of any command  |
| When performing a BAC authentication   |
| When using the CVCA Root key   |
| When verifying a certificate with an extracted key                               |
| When performing the Chip Authentication  |
| When performing a Terminal authentication  |

**Table 7 : TSF Testing**

Dependencies:

FPT\_AMT.1 Abstract machine testing.

**Application note 54:**

The ST writer shall perform the operation in **FPR\_TST.1.1**. If the MRTD's chip uses state of the art smart card technology it will run the some self tests at the request of the authorised user and some self tests automatically. e.g. a self test for the verification of the integrity of stored TSF executable code required by **FPT\_TST.1.3** may be executed during initial start-up by the "authorised user" Manufacturer

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in the Phase 2 Manufacturing. Other self tests may run automatically to detect failure and to preserve of secure state according to **FPT\_FLS.1** in the Phase 4 Operational Use, e.g. to check a calculation with a private key by the reverse calculation with the corresponding public key as countermeasure against Differential Failure Attacks. The security target writer shall perform the operation claimed by the concrete product under evaluation.

### **FPT\_PHP.3 Resistance to physical attack**

FPT\_PHP.3.1 The TSF shall resist **[physical manipulation and physical probing]** to the TSF by responding automatically such that the **[TSP]** is not violated.

Dependencies: No dependencies.

#### **Application note 55:**

The TOE will implement appropriate measures to continuously counter physical manipulation and physical probing. Due to the nature of these attacks (especially manipulation) the TOE can by no means detect attacks on all of its elements. Therefore, permanent protection against these attacks is required ensuring that the TSP could not be violated at any time. Hence, “automatic response” means here

- (i) assuming that there might be an attack at any time and
- (ii) countermeasures are provided at any time.

The following security functional requirements protect the TSF against bypassing. and support the separation of TOE parts.

### **6.3 Security Functional Requirements for the TOE**

The security assurance requirement level is EAL4 augmented with AVA\_VAN.5 and ALC\_DVS.2.

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## 7 TOE SUMMARY SPECIFICATION

This part covers the IT security functionalities and specifies how these functions satisfy the TOE security functional requirement

### 7.1 Security functionality list of the composite TOE

| Identification              | Name                                     |
|-----------------------------|--|
| F.ACC_READ                  | Access control in reading                |
| F.ACC_WRITE                 | Access control in writing                |
| F.BAC                       | BAC mechanism                            |
| F.SM                        | Secure messaging mechanism               |
| F.AUTH_PERSO                | Personalization agent authentication     |
| F.AA                        | Active Authentication                    |
| F.EAC                       | EAC mechanism                            |
| F.SELFTESTs                 | Self tests                               |
| F.ROLLBACK                  | Safe state management                    |
| F.PHYS                      | Physical protection                      |
| IC security functionalities |  |
| F.RNG                       | Random number generator                  |
| F.HW_DES                    | Triple DES coprocessor                   |
| F.HW_AES                    | AES coprocessor                          |
| F.OPC                       | Control of operating conditions          |
| F.PHY                       | Protection against physical manipulation |
| F.LOG                       | Logical protection                       |
| F.COMP                      | Protection of mode control               |
| F.MEM_ACC                   | Memory access control                    |
| F.SFR_ACC                   | Special function register access Control |

**Table 8 : List of the security functionalities of the composite TOE**

### 7.2 Security functionalities provided by the IC

The description of the security functionalities of the IC is provided in [R10], [R11] and [R12].

### 7.3 Security functionalities provided by the TOE

#### **F.ACC\_READ - Access Control in reading**

This function controls access to read functions (in EEPROM) and enforces the security policy for data retrieval.

Prior to any data retrieval, it authenticates the actor trying to access the data, and checks the access conditions are fulfilled as well as the life cycle state.

It ensures that at any time, the keys are never readable:

- BAC keys

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- Chip authentication keys
- CVCA keys
- Active Authentication private key
- Personalisation agent keys

It controls access to the CPLC data as well:

- It ensures the CPLC data can be read during the personalization phase
- It ensures it can not be readable in free mode at the end of the personalization step

Regarding the file structure:

**In the operational use:**

- The terminal can read user data (except DG3 & 4), the Document Security Object, the EF.CVCA, EF.COM only after BAC authentication and through a valid secure channel.
- When the EAC was successfully performed, The terminal can only read the DG3 & 4 provided the access rights are sufficient through a valid secure channel

**In the personalisation phase**

- The personalisation agent can read all the data stored in the TOE after it is authenticated by the TOE (using its authentication keys).
- The TOE is uniquely identified by a random number, generated at each reset. This unique identifier is called (U.I.D)

It ensures as well that no other part of the EEPROM can be accessed at anytime

**F.ACC\_WRITE - Access Control in writing**

This function controls access to write functions (in EEPROM) and enforces the security policy for data writing.

Prior to any data update, it authenticates the actor, and checks the access conditions are fulfilled as well as the life cycle state.

This security functionality ensures the application locks can only be written once in personalization phase to be set to '1'.

It ensures as well the CPLC data can not be written anymore once the TOE is personalized and that it is not possible to load an optional code or change the personalizer authentication keys in personalization phase.

Regarding the file structure

**In the operational use:**

It is not possible to create any files (system or data files). Furthermore, it is not possible to update any system files.

However

- the application data is still accessed **internally** by the application for its own needs

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- the Root CVCA key files and temporary key files are updated internally by the application according to the authentication mechanism described in [R4]

#### **In the personalisation phase**

- The personalisation agent can create and write through a valid secure channel all the data files it needs after it is authenticated by the TOE (using its authentication keys).

#### **F.BAC - BAC mechanism**

This security functionality ensures the BAC is correctly performed. It can only be performed once the TOE is personalized with the symmetric BAC keys the Personalization Agent loaded beforehand during the personalization phase.

Furthermore, this security functionalities ensures the session keys are destroyed at the beginning of each BAC session.

A self-test on TDES and random generator is performed when a BAC session is requested.

It handles an error counter: after several failure in attempting to establish a BAC session (the error limit is reached), the TOE implements countermeasures to protect the TOE : it takes more and more time for the TOE to reply to subsequent wrong BAC attempts.

#### **F.SM - Secure Messaging**

This security functionality ensures the confidentiality & integrity of the channel the TOE and the IFD are using to communicate.

After a successful BAC authentication and successful Chip authentication, a secure channel is (re)established based on Triple DES algorithms.

This security functionality ensures

- No commands were inserted nor deleted within the data flow
- No commands were modified
- The data exchanged remain confidential
- The issuer of the incoming commands and the destination of the outgoing data is the one that was authenticated (through BAC or EAC)

If an error occurs in the secure messaging layer, the session keys are destroyed

#### **F.AUTH\_PERSO - Personalisation Agent Authentication**

This security functionality ensures the TOE, when delivered to the Personalization Agent, demands an authentication prior to any data exchange.

This authentication is based on a symmetric Authentication mechanism based on a Triple DES algorithm.

#### **F.AA - Active Authentication**

This security functionality ensures the Active Authentication is performed as described in [R1] & [R2]. (if it is activated by the personalizer).

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A self-test on the random generator is performed prior to any Active authentication. Moreover, this security functionality is protected against the DFA.

### **F.EAC - EAC mechanism**

This security functionality ensures the EAC is correctly performed. In particular,

- it handles the certificate verification
- the management of access rights to DG3 & DG4
- the management of the current date (update and control towards the expiration date of the incoming certificate)
- the signature verification (in the certificate or in the challenge/response mechanism)

It can only be performed once the TOE is personalized with the chip authentication keys & Root CVCA key(s) the Personalization Agent loaded during the personalization phase.

Furthermore, this security functionalities ensures the authentication is performed as described in [R4].

This security functionalities ensures the session keys for secure messaging are destroyed at each successful Chip Authentication step.

It handles an error counter: after several failure in attempting to strongly authenticate the GIS (the error limit is reached), the TOE implements countermeasures to protect the TOE : it takes more and more time for the TOE to reply to subsequent wrong GIS authentication attempts.

### **F.SELFTESTS - Self tests**

The TOE performs self tests on the TSF data it stores to protect the TOE. In particular, it is in charge of the:

- DFA detection for the Active authentication
- Self tests of the random generator before the BAC and Active Authentication
- Self tests of the DES before the BAC
- Monitoring of the integrity of keys, files and TSF data
- Monitoring the integrity of the optional code (at start up)
- Protecting the cryptographic operation
- .....

The integrity of the files are monitored each time they are accessed and the integrity of the optional code is checked each time the TOE is powered on.

The integrity of keys and sensitive data is checked each time they are used/accessed.

### **F.ROLLBACK - Safe state management**

This security functionalities ensures that the TOE gets back to a secure state when

- an integrity error is detected by F.SELFTESTS
- a tearing occurs (during a copy of data in EEPROM)

This security functionality ensures that such a case occurs, the TOE is either switched in the state "kill card" or becomes mute.

### **F.PHYS – Physical protection**

This security functionality protects the TOE against physical attacks

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## 8 PP CLAIMS

### 8.1 PP reference

The PP EAC [R8] is claimed.

### 8.2 PP refinements

Those refinements (i.e. tailoring) are performed in order to ensure conformance with CC3.1r2.

Some parts of the documents have been removed especially those related to SOF and IT Environment requirements.

FMT\_SEM.1 and FPT\_SEP.1 do not exist any more in CC3.1r2 and have therefore been removed. Nevertheless, they are implicitly required by the ADV\_ARC.1 assurance requirement.

In FPT\_PHP.3, CC3.1r2 have rewrite “The TSP is not violated” by “SFRs are always enforced”. These two sentences are actually equivalent. The SFR has therefore been rewritten to fulfil CC3.1r2.

FMT\_MOF.1 now requires dependencies with FMT\_SMF.1 and FMT\_SMR.1. These dependencies have been added.

FMT\_SMR.1 now requires dependency with FIA\_UID.1. This dependency has been added.

FCS\_CKM.1/KDF\_MRTD has been renamed FCS\_CKM.1/BAC\_MRTD.

### 8.3 PP additions

The additional functionalities are the Active Authentication (AA) based on the ICAO PKI V1.1 and the related on-card generation of RSA and ECC keys. It implies some addition to the standard PP.

The following SFRs are added to the standard PP for the TOE:

- FCS\_COP.1 / SIG\_MRTD
- FIA\_API.1 / AA
- FDP\_ITC / AA
- FMT\_MOF.1 / AA
- FCS\_CKM.1 / ASYM

The following Objective for the TOE is added to the standard PP:

- OT.Chip\_authenticity “Protection against forgery”

The following Objectives for the IT environment are added to the standard PP:

- OE.AA\_Key\_MRTD “Active Authentication key”
- OE.AA\_Personalization “Active Authentication Personalization”

Moreover, the composition with the IC mandates to introduce complementary OSPs:

- P.Plat\_Appl “Development according to the IC recommendations”
- P.Sensitive\_Data\_Protection “Protection of sensitive data”
- P.Key\_Function “Design of the cryptographic routines in order to protect the keys”

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## 9 Rationale

This section presents the evidence to be used for the ST evaluation. This evidence supports the claim that the ST is a complete and cohesive set of requirements and that a conformant TOE would provide an effective set of IT security countermeasures within the security environment.

This rational shows the composition with the IC that is evaluated EAL4+.

### 9.1 Composition with the IC Security target features

#### 9.1.1 Coverage of the assumptions of the IC (A.IC vs TOE)

The assumptions defined in the Security target of the IC are covered by the following OSP and are therefore merged with them:

| IC Assumption  | Covered by                         | Justification  |
|--|------------------------------------|--|
| <b>A.Process-Card</b><br>Protection during Packaging, Finishing and Personalisation  | <b>P.Manufact</b>                  | Security procedures are used during TOE packaging, finishing and prepersonalization (During Phase 2)   |
| <b>A.Plat-Appl</b><br>Usage of Hardware Platform<br>The Smartcard Embedded Software is designed so that the requirements from the following documents are met: <ul style="list-style-type: none"> <li>• TOE guidance documents (refer to the Common Criteria assurance class AGD) such as the hardware data sheet, and the hardware application notes, and</li> <li>• (ii) findings of the TOE evaluation reports relevant for the Smartcard Embedded Software.</li> </ul> | <b>P.Plat-Appl</b>                 | The development of the Smart Card embedded Software was lead in accordance with the recommendations issued by the IC manufacturer. For more details see the "Design Compliance Evidence" |
| <b>A.Resp-Appl</b><br>Treatment of User Data<br>"All User Data are owned by Smartcard Embedded Software. Therefore, it must be assumed that security relevant User Data (especially cryptographic keys) are treated by the Smartcard Embedded Software as defined for the specific application context."   | <b>P.Sensitive_Data_Protection</b> | The Composite TOE ensure the confidentiality of the cryptographic keys it stores   |

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|   |                              |   |
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| <p><b>A.Check-Init</b></p> <p>Check of initialization data by the Smartcard Embedded Software</p> <p>« The Smartcard Embedded Software must provide a function to check Initialisation data. The data is defined by the customer and injected by the TOE Manufacturer into the non-volatile memory to provide the possibility for TOE identification and for traceability”</p>  | <p><b>P.Manufact</b></p>     | <p>Security procedures and manufacturing guidance are used during IC development and production phase (Phase 2)</p>     |
| <p><b>A.Key-Function</b></p> <p>Usage of Key-dependent Functions<br/>« Key-dependent functions (if any) shall be implemented in the Smartcard Embedded Software in a way that they are not susceptible to leakage attacks (as described under T.Leak-Inherent and T.Leak-Forced). Note that here the routines which may compromise keys when being executed are part of the Smartcard Embedded Software. In contrast to this the threats T.Leak-Inherent and T.Leak-Forced address</p> <ul style="list-style-type: none"> <li>the cryptographic routines which are part of the TOE and</li> <li>the processing of User Data including cryptographic keys »</li> </ul> | <p><b>P.Key_Function</b></p> | <p>The Cryptographic routines are designed in such a way that they do not compromise key by any leak of information</p> |

### 9.1.2 Coverage of the environment objectives of the IC (OE.IC vs TOE)

Due to the coverage of related IC assumptions and thanks to transitivity, the environment objectives defined in the Security target of the IC are enforced by the following TOE features:

| Objectives for the environment required by the IC   | Covered by                | Justification   |
|---|---------------------------|---|
| <p><b>OE.Plat-Appl</b></p> <p>Usage of Hardware Platform<br/>The Smartcard Embedded Software is designed so that the requirements from the following documents are met:</p> <ul style="list-style-type: none"> <li>TOE guidance documents (refer to the Common Criteria assurance class AGD) such as the hardware data sheet, and the hardware application notes, and</li> <li>(ii) findings of the TOE evaluation reports relevant for the Smartcard Embedded Software.</li> </ul> | <p><b>P.Plat-Appl</b></p> | <p>The development of the Smart Card embedded Software was lead in accordance with the recommendations issued by the IC manufacturer. For more details see the “Design Compliance Evidence”</p> |

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| <p><b>OE.Resp-Appl</b></p> <p>Treatment of User Data</p> <p>“All User Data are owned by Smartcard Embedded Software. Therefore, it must be assumed that security relevant User Data (especially cryptographic keys) are treated by the Smartcard Embedded Software as defined for the specific application context.”</p>  | <p><b>P.Sensitive_Data_Protection</b></p> | <p>The Composite TOE ensure the confidentiality of the cryptographic keys it stores as well as the integrity of all the sensitive data.</p> |
| <p><b>OE.Process-TOE</b></p> <p>Protection during TOE Development and Production (Phase 2 &amp; 3 of the PP 9911 [R28])</p>   | <p><b>P.Manufact</b></p>                  | <p>This objective is ensured by the security procedures and manufacturing guidelines of NXP manufacturing site</p>                          |
| <p><b>OE.Process-Card</b></p> <p>Protection during Packaging, Finishing and Personalisation</p>   | <p><b>P.Manufact</b></p>                  | <p>Security procedures are used during TOE packaging, finishing and prepersonalization (During Phase 2 of this PP)</p>                      |
| <p><b>OE.Check-Init</b></p> <p>Check of initialization data by the Smartcard Embedded Software</p> <p>« The Smartcard Embedded Software must provide a function to check Initialisation data. The data is defined by the customer and injected by the TOE Manufacturer into the non-volatile memory to provide the possibility for TOE identification and for traceability”</p> | <p><b>P.Manufact</b></p>                  | <p>Security procedures and manufacturing guidance are used during IC development and production phase (Phase 2 of this PP)</p>              |

### 9.1.3 Coverage of the Objectives of the TOE by the objectives of the IC (O.IC vs O.TOE)

The following IC objectives ensure part of the TOE objectives specified in this table. Therefore these TOE objectives are also covered by requirements covering the related IC objectives.

| IC Objectives                     | Ensures  | Covers  |
|-----------------------------------|--|---|
| <p><b>O.Leak-Inherent</b></p>     | <p>Protection against Inherent Information Leakage</p> | <p>OT.Prot_Inf_Leak<br/>OT.Prot_Phys_Tamper</p> |
| <p><b>O.Phys-Probing</b></p>      | <p>Protection against Physical Probing</p>             | <p>OT.Prot_Inf_Leak<br/>OT.Prot_Phys_Tamper</p> |
| <p><b>O.Malfunction</b></p>       | <p>Protection against Malfunctions</p>                 | <p>OT.Prot_Malfunction</p>                      |
| <p><b>O.Phys-Manipulation</b></p> | <p>Protection against Physical Manipulation</p>        | <p>OT.Prot_Inf_Leak<br/>OT.Prot_Phys_Tamper</p> |

|                              |                           |                                 |                     |
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| <b>O.Leak-Forced</b>    | Protection against Forced Information Leakage | OT.Prot_Inf_Leak<br>OT.Prot_Phys_Tamper  |
| <b>O.Abuse-Func</b>     | Protection against Abuse of Functionality     | OT.Prot_Abuse-Func   |
| <b>O.Identification</b> | TOE Identification                            | OT.Identification  |
| <b>O.RND</b>            | Random Numbers                                | OT.Data_Conf<br>OT.Sens_Data_Conf  |
| <b>O.HW_DES3</b>        | Triple DES Functionality                      | OT.AC_Pers<br>OT.Data_Int<br>OT_Data_Conf<br>OT.Sens_Data_Conf                               |
| <b>O.HW_AES</b>         | AES Functionality                             | N/A  |
| <b>O.MF_FW</b>          | MIFARE Firewall                               | N/A  |
| <b>O.MEM_ACCESS</b>     | Area based Memory Access Control              | OT.Prot_Abuse-Fonc<br>OT.Data_Conf<br>OT.Sens_Data_Conf<br>OT.AC_Perso<br>OT.Chip_Auth_Proof |
| <b>O.SFR_ACCESS</b>     | Special Function Register Access Control      | OT.Prot_Abuse-Fonc<br>OT.Data_Conf<br>OT.Sens_Data_Conf<br>OT.AC_Perso<br>OT.Chip_Auth_Proof |
| <b>O.CONFIG</b>         | Protection of configuration data              | OT.Prot_Malfunction<br>OT.Prot_Abuse-Fonc  |

#### 9.1.4 Coverage of the threats of the TOE (T.TOE vs IC.O)

The threats of the TOE are covered by the following IC objectives & assumptions:

| Threats of the TOE    | Covered by  | Justification   |
|-----------------------|---|---|
| T.Chip_ID             | O.Leak-Inherent<br>O.Phys-Probing<br>O.Phys-Manipulation<br>O.Leak-Forced<br>O.Abuse-Func<br>O.Malfunction<br>O.RND | Theses IC objectives ensures <ul style="list-style-type: none"> <li>the MRZ keys used by the TOE can not be disclosed by a physical way (probing, leakage, physical manipulation,..). It ensures the attacker can not read the logical MRTD</li> <li>the authentication can not be replayed by means of a random number.</li> </ul> |
| T.Skimming            | O.Leak-Inherent<br>O.Phys-Probing<br>O.Phys-Manipulation<br>O.Leak-Forced<br>O.Abuse-Func<br>O.Malfunction          | These IC objectives ensures the MRZ keys can not be disclosed by a physical way (probing, leaking, physical manipulation,...)   |
| T.Read_Sensitive_Data | O.RND<br>O.HW_DES3  | O.RND ensures the authentication between the Inspection system and the TOE is unpredictable.<br>O.HW_DES3 ensures the communication are protected in confidentiality and integrity  |
| T.Forgery             | O.Phys-Manipulation<br>O.Abuse-Func<br>O.Malfunction  | O.Phys-Manipulation, O.Abuse-Func and O.Malfunction provide protection against forgery of the Logical MRTD stored in the TOE..  |

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| T.Counterfeit         | O.Leak-Inherent<br>O.Phys-Probing<br>O.Phys-Manipulation<br>O.Leak-Forced<br>O.Abuse-Func<br>O.Malfunction | These objectives ensure that no data may be copied from the TOE  |
| T.Abuse-Func          | O.Mem_Access<br>O.SFR_Access<br>O.Abuse-Func<br>O.Malfunction  | These objectives ensure the functions for personalization and initialization can not be used in operational state. |
| T.Information_Leakage | O.Leak-Inherent<br>O.Phys-Probing<br>O.Phys-Manipulation<br>O.Leak-Forced<br>O.Abuse-Func<br>O.Malfunction | These objectives ensure there is no information leakage  |
| T.Phys_Tamper         | O.Leak-Inherent<br>O.Phys-Probing<br>O.Phys-Manipulation<br>O.Leak-Forced<br>O.Abuse-Func<br>O.Malfunction | These objectives ensure there is no physical tampering   |
| T.Malfunction         | O.Abuse-Func<br>O.Malfunction  | These objectives ensure protection against environmental stress that would lead to a malfunction                   |

## 9.2 Security Objective rationale of the TOE

### 9.2.1 Standard "Extended Access Control" features

The rationale is identical to the one indicated in [R8]

### 9.2.2 Addition for the "Active Authentication" feature

|                       | OT.Chip_authenticity | OE.AA_Personalization | OE.AA_Key_MRTD |
|-----------------------|----------------------|-----------------------|----------------|
| T.Chip-ID             |                      |                       |                |
| T.Skimming            |                      |                       |                |
| T.Read_Sensitive_Data |                      |                       |                |
| T.Forgery             |                      |                       |                |
| T.Counterfeit         | x                    |                       | x              |
| T.Abuse-Func          |                      |                       |                |
| T.Information_Leakage |                      |                       |                |

|                       |                    |                          |              |
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|                   |  |   |   |
|-------------------|--|---|---|
| T.Phys-tamper     |  |   |   |
| T.Malfunction     |  |   |   |
| P.Manufact        |  |   |   |
| P.Personalization |  |   |   |
| P.Personal_Data   |  |   |   |
| P.Sensitive_Data  |  |   |   |
| A.Pers_Agent      |  | x | x |
| A.Insp_Sys        |  |   |   |
| A.Signature_PKI   |  |   |   |
| A.Auth_PKI        |  |   |   |

The assumption **A.Pers\_Agent** "Personalization of the MRTD's chip" is covered by the security objective for the TOE environment **OE.AA\_Personalization** "Active Authentication Personalization" including the enrolment, the protection with digital signature and the storage of the MRTD holder active authentication data (DG15) and the enabling of this security features of the TOE according to the decision of the Issuing State or Organization concerning the Basic Access Control.

The threat **T.Counterfeit** "MRTD's chip" addresses the attack of unauthorized copy or reproduction of the genuine MRTD chip. This attack is thwarted by active authentication proving the authenticity of the chip as required by **OT.Chip\_Authenticity** "Protection against forgery" using a authentication key pair to be generated by the issuing State or Organization. The Public active Authentication Key has to be written into EF.DG15 and signed by means of Documents Security Objects as demanded by **OE.AA\_Key\_MRTD** "Active Authentication Key". MRTDs must be controlled in order to prevent their usage for production of counterfeit MRTDs targeted on by OD.Material.

### 9.3 Security fonctionnal requirements rationale of the TOE

#### 9.3.1 Standard "Extended Access Control" features

The security functional requirements rationales, as well as their justifications are identical to the ones indicated in [R8]

#### 9.3.2 Addition for the "Active Authentication" feature

The rationale binding the SFRs and the TOE objective is described hereafter:

|  |                      |
|--|----------------------|
|  | OT.Chip_Authenticity |
| FAU_SAS.1<br>FCS_CKM.1 /<br>BAC_MRTD<br>FCS_CKM.1 /<br>DH_MRTD<br>FCS_CKM.1 /<br>ASYM<br>FCS_CKM.4 | X                    |

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|                          | OT.Chip_Authenticity |
|--------------------------|----------------------|
| /MRTD                    |                      |
| FCS_COP.1 /<br>SIG_MRTD  | X                    |
| FCS_COP.1 /<br>SHA_MRTD  | X                    |
| FCS_COP.1 /<br>TDES_MRTD |                      |
| FCS_COP.1 /<br>MAC_MRTD  |                      |
| FCS_COP.1 /<br>SIG_VER   |                      |
| FCS_RND.1 /<br>MRTD      | X                    |
| FIA_UID.1                |                      |
| FIA_UAU.1                |                      |
| FIA_API.1 / AA           | x                    |
| FIA_UAU.4 / MRTD         |                      |
| FIA_UAU.5 / MRTD         |                      |
| FIA_UAU.6 / MRTD         |                      |
| FIA_AFL.1                |                      |
| FIA_API.1 / CAP          |                      |
| FDP_ACC.1                | x                    |
| FDP_ACF.1                | x                    |
| FDP_UCT.1 /MRTD          |                      |
| FDP_UIT.1 /MRTD          |                      |
| FDP_ITC / AA             | x                    |
| FMT_MOF.1 / AA           |                      |
| FMT_SMF.1                |                      |
| FMT_SMR.1                |                      |
| FMT_LIM.1                |                      |
| FMT_LIM.2                |                      |
| FMT_MTD.1 /<br>INI_ENA   |                      |
| FMT_MTD.1 /<br>INI_DIS   |                      |
| FMT_MTD.1 /<br>CVCA_INI  |                      |
| FMT_MTD.1 /<br>CVCA_UPD  |                      |
| FMT_MTD.1 / DATE         |                      |
| FMT_MTD.1 /<br>KEY_WRITE | x                    |
| FMT_MTD.1 / CAPK         |                      |
| FMT_MTD.1 /              | x                    |

|                       |                    |                          |              |
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|   |                      |
|---|----------------------|
|   | OT.Chip_Authenticity |
| KEY_READ<br>FMT_MTD.3<br>FPT_EMSEC.1<br>FPT_TST.1<br>FPT_RVM.1<br>FPT_FLS.1<br>FPT_PHP.3<br>FPT_SEP.1 |                      |

The security objective **OT.Chip\_Authenticity** "Protection against forgery" is ensured by the Active Authentication Protocol provided by **FIA\_API.1/AA**, **FDP\_ACC.1** and **FDP\_ACF.1** proving the identity and authenticity of the TOE. The Active Authentication relies on **FCS\_COP.1/SIG\_MRTD**, **FCS\_COP.1/ SHA\_MRTD** and **FCS\_RND.1/MRTD**. It is performed using a TOE internally stored confidential private key as required by **FMT\_MTD.1/KEY\_WRITE** and **FMT\_MTD.1/KEY\_READ**, this key being loaded during personalization phase as required by **FDP\_ITC/AA** or generated on-card by **FCS\_CKM.1/ASYM**.

### 9.3.3 Added dependencies

This table supersedes the PP dependency table:

| Requirements         | Dependencies                                 | Support of the dependencies |
|----------------------|--|-----------------------------|
| FCS_COP.1 / SIG_MRTD | FDP_ITC.1/AA, FCS_CKM.1/ASYM, FCS_CKM.4/MRTD | Fulfilled                   |
| FIA_API.1 / AA       | None   | Fulfilled                   |
| FDP_ITC.1 / AA       | FDP_ACF.1                                    | See Justification 1         |
| FMT_SMR.1            | FIA_UID.1                                    | Fulfilled                   |
| FMT_MOF.1 / AA       | FMT_SMF.1, FMT_SMR.1                         | Fulfilled                   |
| FCS_CKM.1 / ASYM     | FCS_COP.1/SIG_MRTD, FCS_CKM.4/MRTD           | Fulfilled                   |

#### Justification 1:

FMT\_MSA.3 dependency is not required since this import does not involve any specific security attributes.

## 9.4 Security functionalities/SFRs mapping

The following section maps Security Functionalities supplied by the TOE [R7]/[R8] to Security Functional Requirements.

|                       |                    |                          |              |
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|                       | Security functionalities of the TOE |             |       |      |              |      |       |             |            |        | Security functionalities of the IC |          |                     |       |       |       |        |           |                      |
|-----------------------|-------------------------------------|-------------|-------|------|--------------|------|-------|-------------|------------|--------|------------------------------------|----------|---------------------|-------|-------|-------|--------|-----------|----------------------|
|                       | F.ACC_READ                          | F.ACC_WRITE | F.BAC | F.SM | F.AUTH_PERSO | F.AA | F.EAC | F.SELFTESTS | F.ROLLBACK | F.PHYS | F.RNG                              | F.HW_DES | F.HW_AES / not used | F.OPC | F.PHY | F.LOG | F.COMP | F.MEM_ACC | F.SFR_ACC / not used |
| FAU_SAS.1             | x                                   | x           |       |      |              |      |       |             |            |        |                                    |          |                     |       |       |       | X      |           |                      |
| FCS_CKM.1 / BAC_MRTD  |                                     |             | x     |      |              |      |       |             |            |        | x                                  | X        |                     |       |       |       |        |           |                      |
| FCS_CKM.1 / DH_MRTD   |                                     |             |       |      |              |      | X     |             |            |        |                                    |          |                     |       |       |       |        |           |                      |
| FCS_CKM.1 / ASYM      |                                     |             |       |      |              | X    |       |             |            |        |                                    |          |                     |       |       |       |        |           |                      |
| FCS_CKM.4 /MRTD       |                                     |             | x     | x    |              |      | X     |             |            |        |                                    |          |                     |       |       |       |        |           |                      |
| FCS_COP.1 / SIG_MRTD  |                                     |             |       |      |              | x    |       |             |            |        |                                    |          |                     |       |       |       |        |           |                      |
| FCS_COP.1 / SHA_MRTD  |                                     |             | x     |      |              |      | X     |             |            |        |                                    |          |                     |       |       |       |        |           |                      |
| FCS_COP.1 / TDES_MRTD |                                     |             |       | x    |              |      |       |             |            |        | X                                  |          |                     |       |       |       |        |           |                      |
| FCS_COP.1 / MAC_MRTD  |                                     |             |       | x    |              |      |       |             |            |        | X                                  |          |                     |       |       |       |        |           |                      |
| FCS_COP.1 / SIG_VER   |                                     |             |       |      |              |      | X     |             |            |        |                                    |          |                     |       |       |       |        |           |                      |
| FCS_RND.1 / MRTD      | x                                   |             | x     |      |              | x    | X     |             |            | X      |                                    |          |                     |       |       |       |        |           |                      |
| FIA_UID.1             |                                     |             | x     |      | x            |      |       |             |            | X      | X                                  |          |                     |       |       |       |        |           |                      |
| FIA_UAU.1             |                                     |             | x     |      | x            |      |       |             |            | X      | X                                  |          |                     |       |       |       |        |           |                      |
| FIA_API.1 / AA        |                                     |             |       |      |              | x    |       |             |            | x      | x                                  |          |                     |       |       |       |        |           |                      |
| FIA_UAU.4 / MRTD      |                                     |             | x     |      | x            |      | X     |             |            | X      | X                                  |          |                     |       |       |       |        |           |                      |
| FIA_UAU.5 / MRTD      |                                     |             | x     | x    | x            |      | X     |             |            | x      | X                                  |          |                     |       |       |       | X      |           |                      |
| FIA_UAU.6 / MRTD      |                                     |             |       | x    |              |      |       |             |            |        | X                                  |          |                     |       |       |       |        |           |                      |
| FIA_AFL.1             |                                     |             | x     |      |              |      | X     |             |            |        |                                    |          |                     |       |       |       |        |           |                      |
| FIA_API.1 / CAP       |                                     |             |       |      |              |      | x     |             |            |        |                                    |          |                     |       |       |       |        |           |                      |
| FDP_ACC.1             | x                                   | x           |       |      |              |      |       |             |            |        |                                    |          |                     |       |       |       |        | X         |                      |
| FDP_ACF.1             | x                                   | x           |       |      |              |      |       |             |            |        |                                    |          |                     |       |       |       |        | X         |                      |
| FDP_UCT.1 /MRTD       |                                     |             |       | X    |              |      |       |             |            |        | X                                  |          |                     |       |       |       |        |           |                      |
| FDP_UIT.1 /MRTD       |                                     |             |       | X    |              |      |       |             |            |        | X                                  |          |                     |       |       |       |        |           |                      |
| FDP_ITC / AA          |                                     | X           |       |      |              | X    |       |             |            |        |                                    |          |                     |       |       |       |        | X         |                      |
| FMT_MOF.1 / AA        |                                     | x           |       |      |              | X    |       |             |            |        |                                    |          |                     |       |       |       | X      | X         |                      |
| FMT_SMF.1             |                                     | x           |       |      |              |      |       |             |            |        |                                    |          |                     |       |       |       | X      | X         |                      |
| FMT_SMR.1             |                                     |             | x     |      | x            |      | X     |             |            | X      | x                                  |          |                     |       |       |       |        | X         |                      |

|                       | Security functionalities of the TOE |             |       |      |              |      |       |             |            | Security functionalities of the IC |       |          |                     |       |       |       |        |           |                      |
|-----------------------|-------------------------------------|-------------|-------|------|--------------|------|-------|-------------|------------|------------------------------------|-------|----------|---------------------|-------|-------|-------|--------|-----------|----------------------|
|                       | F.ACC_READ                          | F.ACC_WRITE | F.BAC | F.SM | F.AUTH_PERSO | F.AA | F.EAC | F.SELFTESTS | F.ROLLBACK | F.PHYS                             | F.RNG | F.HW_DES | F.HW_AES / not used | F.OPC | F.PHY | F.LOG | F.COMP | F.MEM_ACC | F.SFR_ACC / not used |
| FMT_LIM.1             | X                                   |             |       |      |              |      |       | X           |            | X                                  |       |          |                     |       |       |       |        |           |                      |
| FMT_LIM.2             | x                                   |             |       |      |              |      |       | X           |            | X                                  |       |          |                     |       | X     |       |        |           |                      |
| FMT_MTD.1 / INI_ENA   |                                     | X           |       |      |              |      |       |             |            |                                    |       |          |                     |       |       |       |        | X         |                      |
| FMT_MTD.1 / INI_DIS   |                                     | X           |       |      |              |      |       |             |            |                                    |       |          |                     |       |       |       | X      | X         |                      |
| FMT_MTD.1 / CVCA_INI  |                                     | X           |       |      |              |      |       |             |            |                                    |       |          |                     |       |       |       | X      | X         |                      |
| FMT_MTD.1 / CVCA_UPD  |                                     | X           |       |      |              |      |       |             |            |                                    |       |          |                     |       |       |       | X      | X         |                      |
| FMT_MTD.1 / DATE      |                                     | x           |       |      |              |      |       |             |            |                                    |       |          |                     |       |       |       |        | x         |                      |
| FMT_MTD.1 / KEY_WRITE |                                     | X           |       |      |              |      |       |             |            |                                    |       |          |                     |       |       |       |        | X         |                      |
| FMT_MTD.1 / CAPK      |                                     | X           |       |      |              |      |       |             |            |                                    |       |          |                     |       |       |       | X      | x         |                      |
| FMT_MTD.1 / KEY_READ  | X                                   |             |       |      |              |      |       |             |            |                                    |       |          |                     |       |       |       |        | X         |                      |
| FMT_MTD.3             |                                     |             |       |      |              |      | x     |             |            |                                    |       |          |                     |       |       |       |        |           |                      |
| FPT_EMSEC.1           | x                                   | X           | x     | x    | x            | x    | x     |             |            | X                                  |       |          |                     | X     | x     | X     |        |           |                      |
| FPT_TST.1             |                                     |             |       |      |              |      |       | X           |            |                                    |       |          |                     |       |       |       |        |           |                      |
| FPT_FLS.1             |                                     |             |       |      |              |      |       |             | x          |                                    |       |          |                     | x     |       |       |        |           |                      |
| FPT_PHP.3             |                                     |             |       |      |              |      |       |             |            | x                                  |       |          |                     | X     | X     | X     |        |           |                      |

Table 9 : Mapping of the security functionalities of the TOE vs the SFRs

## 9.5 Security Assurance requirements rationale

A security assurance requirement rationale for the EAL4+ level is provided in [R8]. This rationale is still relevant for CC3.1r2 considering:

- EAL4 levels are equivalent,
- ALC\_DVS.2 augmentations are equivalent,
- AVA\_VLA.4 is equivalent to AVA\_VAN.5.

Moreover, as the underlying IC is certified according to [R6] with level EAL5+, the composition is straight forward.

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## 10 References

### MRTD specifications

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- [R2] Machine readable Travel Documents – Supplements 9303
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- [R4] Advanced Security Mechanisms for Machine readable travel documents – Extended Access control (EAC) – TR03110 – v1.11
- [R5] ANNEX to Section III SECURITY STANDARDS FOR MACHINE READABLE TRAVEL DOCUMENTS Excerpts from ICAO Doc 9303, Part 1 - Machine Readable Passports, Fifth Edition – 2003

### Protection Profiles

- [R6] Smartcard IC Platform Protection Profile v 1.0 - BSI-PP-0002-2001 Jul 2001
- [R7] Machine readable travel documents with “ICAO Application”, Basic Access control – BSI-PP-0017 v1.0
- [R8] Machine readable travel documents with “ICAO Application”, Extended Access control – BSI-PP-0026 v1.2
- [R9] E-passport: adaptation and interpretation of e-passport Protection Profiles, SGDN/DCSSI/SDR, ref. 10.0.1, February 2007

### Security Targets

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- [R11] Security Target Lite, Evaluation of the NXP P5CD080V0B, P5CN080V0B, P5CC080V0Bn P5CC073V0B Security Target Lite v1.1 -May 9<sup>th</sup> 2007
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### Standards

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- [R15] ISO/IEC 15946-1. Information technology – Security techniques – Cryptographic techniques based on elliptic curves – Part 1: General, 2002
- [R16] ISO/IEC 15946-2. Information technology – Security techniques – Cryptographic techniques based on elliptic curves – Part 2: Digital signatures, 2002

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|-------|--|
| AA    | Active Authentication                      |
| BAC   | Basic Access Control                       |
| CA    | Chip authentication                        |
| CC    | Common Criteria Version 3.1 revision       |
| CPLC  | Card personalisation life cycle            |
| CVCA  | Country Verifying Certification Authority  |
| DF    | Dedicated File                             |
| DFA   | Differential Fault Analysis                |
| DG    | Data Group                                 |
| EAC   | Extended Access Control                    |
| EAL   | Evaluation Assurance Level                 |
| ECC   | Elliptic curve cryptography                |
| ECDH  | Elliptic curve Diffie Hellmann             |
| ECDSA | Elliptic curve Digital signature Algorithm |
| EF    | Elementary File                            |
| EFID  | File Identifier                            |
| DES   | Digital encryption standard                |
| DH    | Diffie Hellmann                            |
| I/O   | Input/Output                               |
| IC    | Integrated Circuit                         |
| ICAO  | International Civil Aviation organization  |
| ICC   | Integrated Circuit Card                    |
| IFD   | Interface device                           |
| LDS   | Logical Data structure                     |
| MF    | Master File                                |
| MRTD  | Machine readable Travel Document           |
| MRZ   | Machine readable Zone                      |
| MSK   | Manufacturer Secret Key                    |
| OS    | Operating System                           |
| PKI   | Public Key Infrastructure                  |
| PP    | Protection Profile                         |
| SFI   | Short File identifier                      |
| SHA   | Secure hashing Algorithm                   |
| SOD   | Security object Data                       |
| TA    | Terminal Authentication                    |
| TOE   | Target of Evaluation                       |
| TSF   | TOE Security function                      |